

SOF220

Quad ADC 12-bit @ 1.6 GSPS, SOFI Module



SOF220

Key Features

- Quad ADC12SJ1600 ADC 12-bit at 1.6GSPS
- Full-Power input bandwidth: 6 GHz
- 4 JESD204C lanes from each ADC
- Onboard Wideband PLL
- LMK04832 PLL (with 100 MHz VCXO)

The board has quad TI ADC12SJ1600 which provides 12-bit resolution at 1.6 GSPS. This ADC is ideal for light detection and ranging (LiDAR) systems. The full-power input bandwidth also enables direct RF sampling of L-band and S-band.

The FMC254 provides four single ADC to allow interleaving among the four ADC. This is accomplished by skewing the sampling clock property among the four ADC.

The four analog input, clock and trigger interfaces of the SOF220 are routed via SSMC connectors. The internal clock frequency is programmable and the clock is capable of locking to an external reference.

NOTE: This module is not orderable as a standalone item and must be purchased as part of an AMSxxx (AMC module) or VPSxxx (3U VPX module) product. See [SOFI Overview](#) for details.



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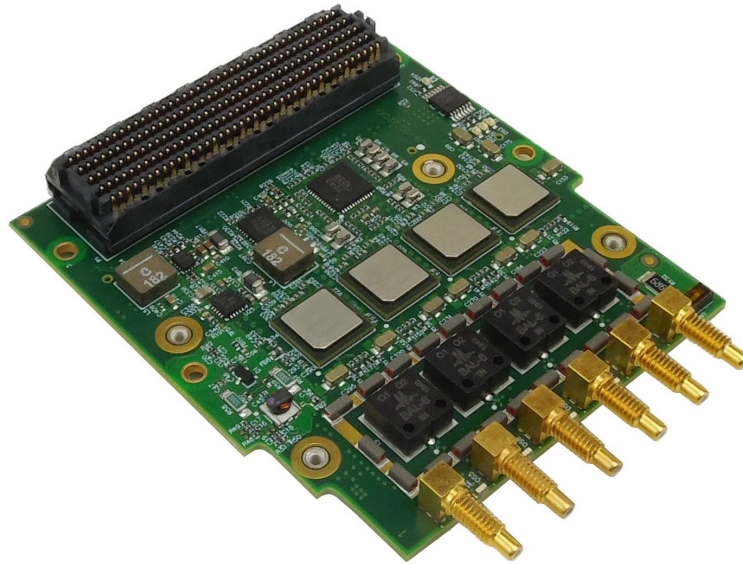


Figure 1: SOF220

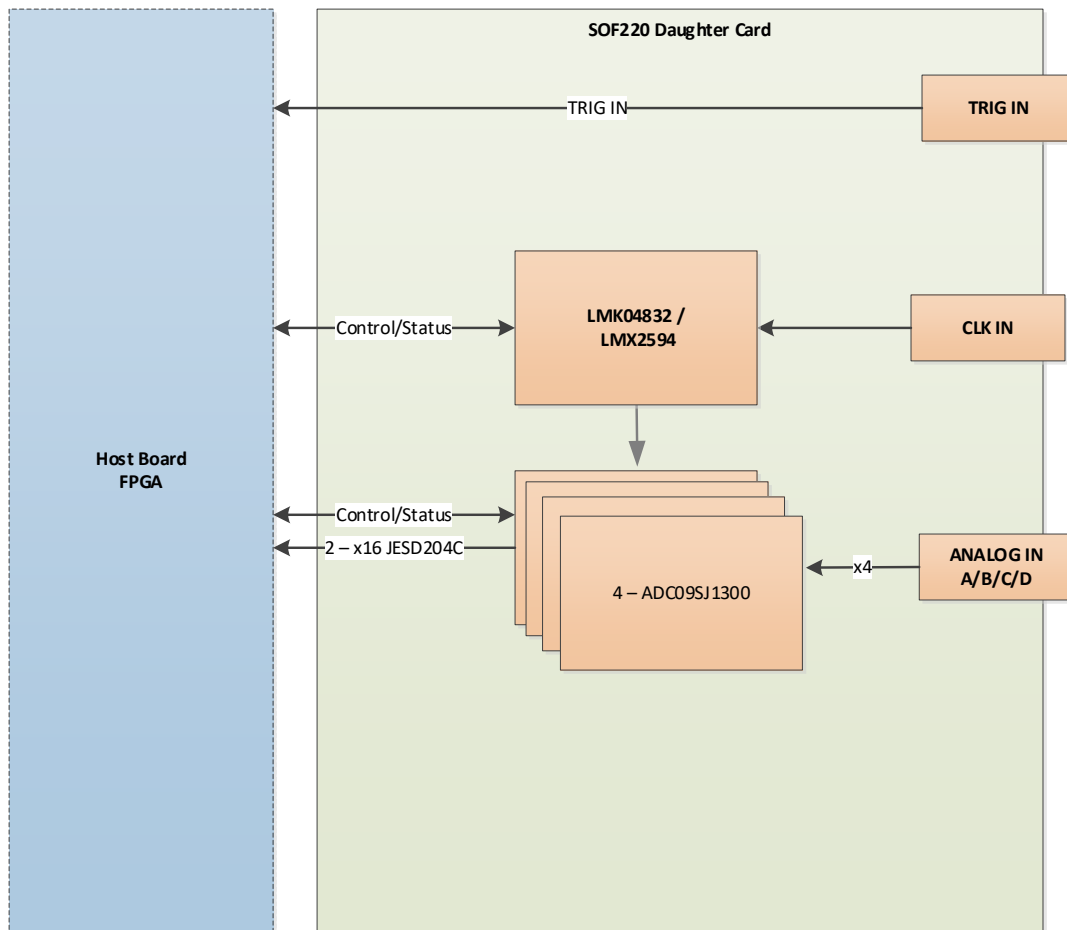


Figure 2: SOF220 Functional Block Diagram

Contact

VadaTech Corporate Office

198 N. Gibson Road, Henderson, NV 89014

Phone: +1 702 896-3337 | Fax: +1 702 896-0332

Asia Pacific Sales Office

7 Floor, No. 2, Wenhui Street, Neihu District, Taipei 114, Taiwan

Phone: +886-2-2627-7655 | Fax: +886-2-2627-7792

VadaTech European Sales Office

VadaTech House, Bulls Copse Road, Southampton, SO40 9LR

Phone: +44 2380 016403

info@vadatech.com | www.vadatech.com

SOFI Modules

SOFI modules are physically similar to FMC modules but are incompatible with FMC carriers, and not designed to be interchangeable in the field.

See [SOFI Overview](#) for compatibility with AMC and 3U VPX carriers. If the particular combination of form factor, FPGA and ADC/DAC performance you require is not yet listed, please contact your local VadaTech sales team for details of how we can support you.

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