VPX983

Chassis Manager carrier for 6U VPX based on VadaTech VT042 Module



Key Features

- Quad ARM Cortex-A53 @ 1.6 GHz per core
- 4GB LP-DDR4 memory
- FRAM for log messages
- 64 NAND Flash
- I2C Real Time Clock with battery backup
- Low power (2W)
- Based on the VadaTech VT042 Module
- IPMI 2.0 compliant

openVP

Benefits

- Supports VITA 46.11 Tier-2 command set
- Utilizing VadaTech VT042 fourth generation Shelf
- Electrical, mechanical, software, and system-level expertise in house
- Full system supply from industry leader
- AS9100 and ISO9001 certified company



vadatech

VPX983

The VadaTech VPX983 Chassis Manager is based on the VITA 46.11 specification. The VITA 46.11 leverages the Intelligent Platform Management Interface (IPMI) and AdvancedTCA Specification by PICMG as its architectural foundation. The VPX983 is a carrier for VadaTech VT042 Module which provides all the health management to the VPX system. <u>VT042 - System Management for VPX Chassis, 4th Generation (vadatech.com)</u>

The VadaTech Chassis Management solution is derived from our field proven VadaTech ATCA Shelf Manager utilizing core interfaces such as the Simple Network Management Protocol (SNMP), Remote Management Control Protocol (RMCP), Web Interface, System Management application (Scorpionware™), and a user-friendly Command Line Interface.

VadaTech VPX IPMC and Chassis Management solutions support VITA 46.11 Tier-2 command set, providing a higher level of functionality in the management layer and chassis cooling capabilities. Additionally, VadaTech VPX management solutions have taken advantage of the HPM.1 PICMG Specification providing a framework for upgrading the IPMC firmware.

The Module has GbE as 1000Base-TX to the front panel connecting to the Shelf Manager or as GbE to P1 as 1000Base-BX (SERDES Based). The module also had 10/100 Ethernet port in the front as well as 10/100 Ethernet going to P2 connector for fail over between two VPX983. The P2 connector also carries further signals for fail over between the two modules for full fail over.

The module allows the P1 and/or P2 to be installed as load option for chassis that don't route the signals.



Figure A: VPX983



Figure B: VPX983 Top View

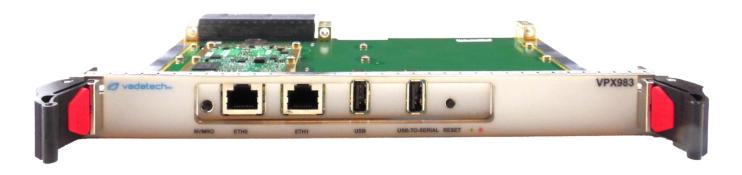


Figure C: VPX983 Front Panel View

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Scorpionware[™] Software

VadaTech's Scorpionware[™] software can be used to access information about the current state of the Shelf or the Carrier, obtain information such as the FRU population, or monitor alarms, power management, current sensor values, and the overall health of the Shelf. The software GUI is very powerful, providing a Virtual Carrier and FRU construct for a simple, effective interface.

IPMI Protocol Analyzer

	Platform Event && Req	uest			•	Expres:	sion Apply		
No.	Time	Bus	Dir	Src	Dest	Seq	Net Fn	Command	
722	77.050.000	IPMB-A	REQ	0×92		16	Sensor/Event	Platform Event	
724	77.330.000	IPMB-A	REQ	0×88	0×20	1	Sensor/Event	Platform Event	
725	77.410.000	IPMB-A	REQ	0×90		20	Sensor/Event	Platform Event	
728	77.740.000	IPMB-B	REQ	0×88		2	Sensor/Event	Platform Event	
729	77.810.000	IPMB-B	REQ	0×92		20	Sensor/Event	Platform Event	
730	77.830.000	IPMB-A	REQ	0x92		8	Sensor/Event	Platform Event	
731	77.840.000	IPMB-B	REQ	0×92		12	Sensor/Event	Platform Event	
732	77.870.000	IPMB-A	REQ	0x92		16	Sensor/Event	Platform Event	
735 736	78.210.000	IPMB-A IPMB-B	REQ	0×88 0×90		3 20	Sensor/Event	Platform Event	
736	78.230.000 78.610.000	IPMB-B IPMB-B	REQ REQ	0x90		4	Sensor/Event Sensor/Event	Platform Event Platform Event	
739	78.640.000	IPMB-B	REQ	0x00		20	Sensor/Event	Platform Event	
739	78.650.000	IPMB-6	REQ	0x92 0x92		8	Sensor/Event	Platform Event	
741	78.660.000	IPMB-B	REQ	0x92		12	Sensor/Event	Platform Event	
742	78.690.000	IPMB-A	REQ	0x92		16	Sensor/Event	Platform Event	
743	79.020.000	IPMB-A	REO	0×88		5	Sensor/Event	Platform Event	
744	79.050.000	IPMB-A	REQ	0×90		20	Sensor/Event	Platform Event	
745	79.430.000	IPMB-B	REQ	0×88		6	Sensor/Event	Platform Event	
746 4	79.460.000	TPMB-8	REO	0x92	0×20	20	SensoriEvent	Platform Event	
	equest: 0x88 - Header	-> Ox2O Plat	form	Event	(Sens	or/Eve	ent) (seq 2)		
ŀ	- Header - Body - Event Mess - Sensor Typ - Sensor Num - Event Type - Event Dire - Offset - Byte 2 Enc	age Revision ber : : :ction	1 : 0 : 0 : 0 : 0 : 0 : 0)x04)x01)x02)x01)x01)x01)x07)x01	(4) (Tempers (2) (Thresh (Deasser (Upper) (Trigger	ature) old) rtion) Non-Cr r Read	ritical Going Ning)	g High)	
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VPX983 can be used as an IPMI protocol analyzer. Figure shows the trace viewer output from VPX983.

Figure 1: IPMI Protocol Analyzer Trace Viewer Output

Block Diagram

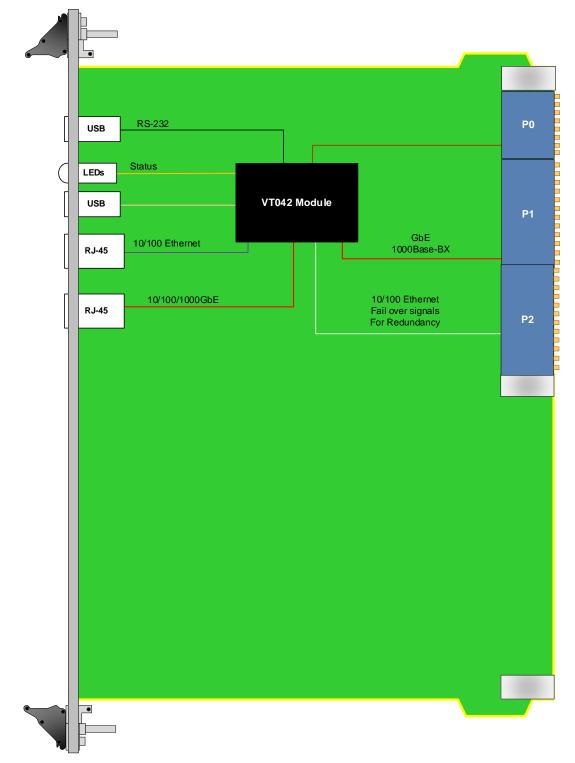


Figure 2: VPX983 Functional Block Diagram

Pinout Block Diagram

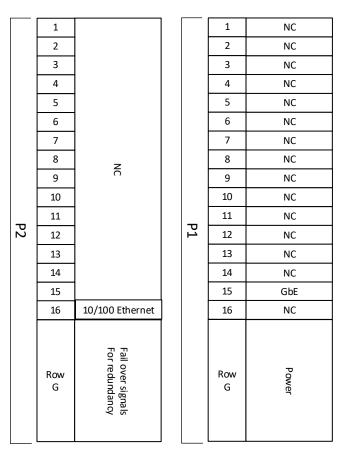


Figure 3: VPX983 Pinout Block Diagram

Specifications

Architecture		
Physical	Dimensions	6U, 1" pitch
Standards		
Module Management	IPMI	VadaTech VPX Shelf Manager and JTAG Switch Module
Configuration		
Power	VPX983	~2W
Environmental	Temperature	See Ordering Options
Other		
MTBF	MIL Hand book 217-F@ TI	BD hrs
Certifications	Designed to meet FCC, CE	E and UL certifications, where applicable
Standards	VadaTech is certified to bo	th the ISO9001:2015 and AS9100D standards
Warranty	Two (2) years, see VadaTe	ech Terms and Conditions

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as preconfigured Application-Ready Platforms. Please contact VadaTech Sales for more information.

Ordering Options

VPX983 - ABC-000-GHJ

A = VPX P1 Connector	G = Applicable Slot Profiles
0 = Not installed 1 = Installed	0 = 5 HP, VITA 48.1
B = VPX P2 Connector	H = Environmental
0 = No installed 1 = Installed	See Environmental Specification
C = VPX Connector Type	J = Conformal Coating
0 = Standard 50u Gold Rugged 1 = KVPX Connectors	0 = No coating 1 = Humiseal 1A33 Polyurethane 2 = Humiseal 1B31 Acrylic

Notes:

Environmental Specification

	Air Cooled		Conduction Cooled			
Option H	H = 0	H = 1	H = 2	H = 3	H = 4	
Operating Temperature	AC1* (0°C to +55°C)	AC3* (-40°C to +70°C)	CC1* (0°C to +55°C)	CC3* (-40°C to +70°C)	CC4* (-40°C to +85°C)	
Storage Temperature	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C3* (-50°C to +100°C)	
Operating Vibration	V2* (0.04 g2/Hz max)	V2* (0.04 g2/Hz max)	V3* (0.1 g2/Hz max)	V3* (0.1 g2/Hz max)	V3 (0.1 g2/Hz max)	
Storage Vibration	OS1* (20g)	OS1* (20g)	OS2* (40g)	OS2* (40g)	OS2* (40g)	
Humidity	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing	

Notes: *Nomenclature per ANSI/VITA 47. Contact local sales office for conduction cooled (H = 2, 3, 4).

Related Products

VPX551



- Dual Kintex UltraScale™ XCKU115
- 16 GB of 64-bit wide DDR4 Memory to each FPGA
- Rear fibre I/O via VITA 66.5

VTX866



- 11U VPX Chassis, Twelve 6U Slots with RTM Support
- Dual Dedicated Switch/management slots
- Up to ten 6U VPX payload slots (with two slots that can have up to 10 HP)

VPX752



- 6U VPX module Intel 5th Generation Xeon-D SoC
- PCle Gen3 x16 (dual x8 or quad x4)
- Quad 10GbE XAUI

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