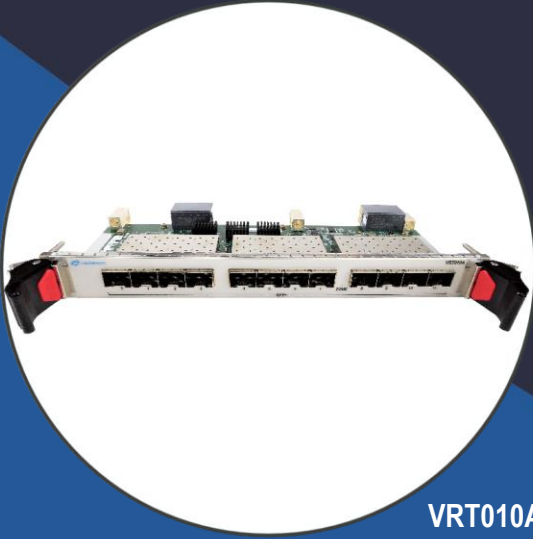


VRT010A

Rear Transition Module with 12
SFP+ ports for 6U VPX



VRT010A

Key Features

- 6U RTM VITA 46
- 12x SFP+ Ports
- Interfaces with the VadaTech VPX010 100G switch

Benefits

- Network software support layer 3
- Full System supply from industry leader
- AS9100 and ISO9001 certified company

OpenVPX™



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VRT010A

The VRT010A is a 6U VPX Rear Transition Module for use with the VPX010. The module has 12 x SFP+ ports. The module is fully supported by VadaTech software layer switch (VPX010 product). The SFP+ ports could run at 1G and/or 10G speed. The ports are configurable by the software.



Figure 1: VRT010A

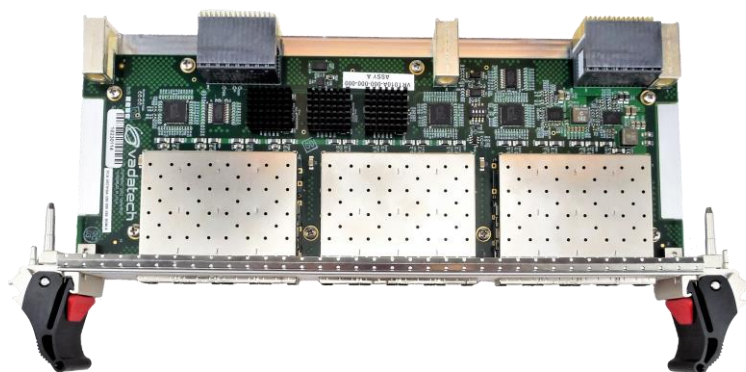


Figure 2: VRT010A Top View



Figure 3: VRT010A Front Panel View

Block Diagram

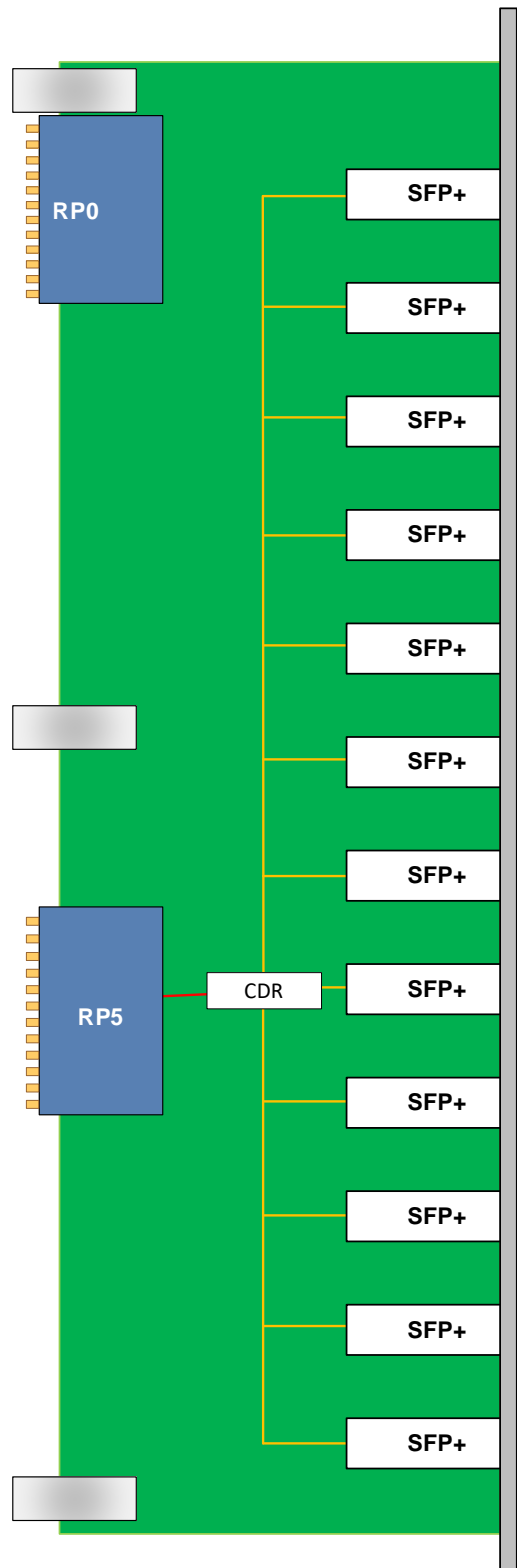


Figure 4: VRT010A Functional Block Diagram

Specifications

Architecture		
Physical	Dimensions	6U, 1" pitch
FPGA		VPX RTM
Configuration		
Power	VRT010A	4W (without Fibre Transceivers)
Memory		None
Rear Panel		12x SFP+
VPX Interfaces	Slot Profiles	See Ordering Options
Rear IO	RP5	12x 10G
	Power Supplies	On RP0: 12V
Other		
MTBF		MIL Hand book 217-F@ TBD hrs
Certifications		Designed to meet FCC, CE and UL certifications, where applicable
Standards		VadaTech is certified to both the ISO9001:2015 and AS9100D standards
Warranty		Two (2) years, see VadaTech Terms and Conditions

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.

Ordering Options

VRT010A – A00-000-GHJ

A = SFP+ Transceivers*		G = Applicable Slot Profiles
0 = None 1 = SR 2 = LR 3 = Reserved 4 = Reserved		0 = 5 HP
		H = Environmental
		See Environmental Specification
		J = Conformal Coating
		0 = No coating 1 = Humiseal 1A33 Polyurethane 2 = Humiseal 1B31 Acrylic

Notes:

*All 12 Transceivers will be the same. Contact local sales office for mix of options.

Environmental Specification

	Air Cooled		Conduction Cooled		
Option H	H = 0	H = 1	H = 2	H = 3	H = 4
Operating Temperature	AC1* (0°C to +55°C)	AC3* (-40°C to +70°C)	CC1* (0°C to +55°C)	CC3* (-40°C to +70°C)	CC4* (-40°C to +85°C)
Storage Temperature	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C3* (-50°C to +100°C)
Operating Vibration	V2* (0.04 g2/Hz max)	V2* (0.04 g2/Hz max)	V3* (0.1 g2/Hz max)	V3* (0.1 g2/Hz max)	V3 (0.1 g2/Hz max)
Storage Vibration	OS1* (20g)	OS1* (20g)	OS2* (40g)	OS2* (40g)	OS2* (40g)
Humidity	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing

Notes:

*Nomenclature per ANSI/VITA 47. Contact local sales office for conduction cooled (H = 2, 3, 4)

Related Products

VPX516



- 3U FPGA carrier for FPGA Mezzanine Card (FMC) per VITA 46 and VITA 57
- Xilinx Virtex-7 690T FPGA in FFG-1761 package
- High-performance clock jitter cleaner

VPX517



- 3U FPGA carrier for FMC per VITA 46 and VITA 57
- Xilinx Kintex-7 410T FPGA in FFG-900 package
- High-performance clock jitter cleaner

Contact

VadaTech Corporate Office

198 N. Gibson Road, Henderson, NV 89014

Phone: +1 702 896-3337 | Fax: +1 702 896-0332

Asia Pacific Sales Office

7 Floor, No. 2, Wenhua Street, Neihu District, Taipei 114, Taiwan

Phone: +886-2-2627-7655 | Fax: +886-2-2627-7792

VadaTech European Sales Office

VadaTech House, Bulls Copse Road, Southampton, SO40 9LR

Phone: +44 2380 016403

info@vadatech.com | www.vadatech.com

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