VRT518A

Rear I/O for VPX518, VPX RTM



Key Features

- 3U RTM per VITA 46
- Dual GbE, Clock input and I/O

Benefits

- Electrical, mechanical, software, and system-level expertise in house
- Full system supply from industry leader
- AS9100 and ISO9001 certified company





VRT518A

The VRT518A is a 3U VPX Rear Transition Module providing I/O expansion for use with the VPX518.

Dual GbE (UTP) are routed from RP1 to back panel RJ-45. The 1PPS input signal is routed from the backplane to the RTM.

The high speed I/O connector supports general purpose and serial interfaces routed from RP2 LVDS. This section of the board can be customized for specific customer requirements, contact VadaTech sales for details.



Figure 1: VRT518A

Block Diagram

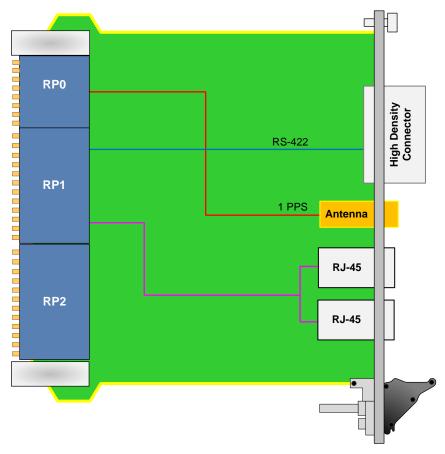


Figure 2: VRT518A Functional Block Diagram

Back Panel

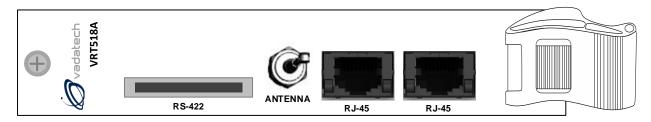


Figure 3: VRT518A Back Panel

Specifications

Architecture					
Physical	Dimensions	3U RTM, 1" pitch			
Configuration					
Power	VRT518A	3W			
Rear Panel	Connectors	1 PPS, Co-ax			
		Dual GbE, RJ-45			
		GPIO/Serial, Oculink			
VPX Interfaces	Slot Profiles	See Ordering Options			
	Backplane	RP0: 1 PPS			
		RP1: Dual GbE			
		RP1: I/O			
	Power Supplies	RP0: VS1 = 12V			
Other					
MTBF	MIL Hand book 217-F	MIL Hand book 217-F@ TBD hrs			
Certifications	Designed to meet FC0	Designed to meet FCC, CE and UL certifications, where applicable			
Standards	VadaTech is certified	VadaTech is certified to both the ISO9001:2000 and AS9100B:2004 standards			
Warranty	Two (2) years, see Va	Two (2) years, see VadaTech Terms and Conditions			

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as preconfigured Application-Ready Platforms. Please contact VadaTech Sales for more information.

Ordering Options

VRT518A - 000-000-GHJ

	G = Applicable Slot Profiles
	0 = 5 HP
	H = Environmental
	See Environmental Specification
	J = Conformal Coating
	0 = No coating 1 = Humiseal 1A33 Polyurethane 2 = Humiseal 1B31 Acrylic

Environmental Specification

Air Cooled			Conduction Cooled		
Option H	H = 0	H = 1	H = 2	H = 3	H = 4
Operating Temperature	AC1* (0°C to +55°C)	AC3* (-40°C to +70°C)	CC1* (0°C to +55°C)	CC3* (-40°C to +70°C)	CC4* (-40°C to +85°C)
Storage Temperature	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C3* (-50°C to +100°C)
Operating Vibration	V2* (0.04 g2/Hz max)	V2* (0.04 g2/Hz max)	V3* (0.1 g2/Hz max)	V3* (0.1 g2/Hz max)	V3 (0.1 g2/Hz max)
Storage Vibration	OS1* (20g)	OS1* (20g)	OS2* (40g)	OS2* (40g)	OS2* (40g)
Humidity	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing

Notes: *Nomenclature per ANSI/VITA 47. Contact local sales office for conduction cooled (H = 2, 3, 4)

Related Products





- 3U FPGA carrier for FPGA Mezzanine Card (FMC) per VITA 46 and VITA 57
- Xilinx Zynq-7000 FPGA in FFG-900 package (XC7Z100 or XC7Z045)
- Protocols such as PCle, SRIO, 10GbE/40Gbe, etc. are FPGA programmable

VPX754



- 3U VPX module Intel 5th Generation Xeon D-1577, D-1548 or D-1520 (Broadwell) System-on-Chip (SoC)
- PCIe Gen3 dual x4 or single x8
- Front-panel video out via micro HDMI

VTX870



- Open VPX benchtop development platform
- Dedicated Switch/management slot
- Up to five 3U VPX payload slots

Contact

VadaTech Corporate Office

198 N. Gibson Road, Henderson, NV 89014 Phone: +1 702 896-3337 | Fax: +1 702 896-0332

Asia Pacific Sales Office

7 Floor, No. 2, Wenhu Street, Neihu District, Taipei 114, Taiwan Phone: +886-2-2627-7655 | Fax: +886-2-2627-7792

VadaTech European Sales Office

VadaTech House, Bulls Copse Road, Southampton, SO40 9LR Phone: +44 2380 016403

info@vadatech.com | www.vadatech.com

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- · Accelerated deployment
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