VRT762A

Rear Transition Module I/O for VadaTech VPX762

VRT762A

Key Features

- 6U RTM VITA 46
- Dual M.2 NVMe Storage
- Dual GbE via RJ-45
- Interfaces to the VadaTech VPX762
- Dual RS-232

Benefits

- Full System supply from industry leader
- AS9100 and ISO9001 certified company

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VRT762A

The VRT762A is a 6U VPX Rear Transition Module for use with VadaTech VPX562. The module has a dual M.2 NVMe storage, dual GbE via RJ-45 and also dual RS-232 (one to the health management and one to the payload).

Figure 1: VRT762A

Figure 2: VRT762A Top View

Figure 3: VRT762A Front Panel View

Block Diagram

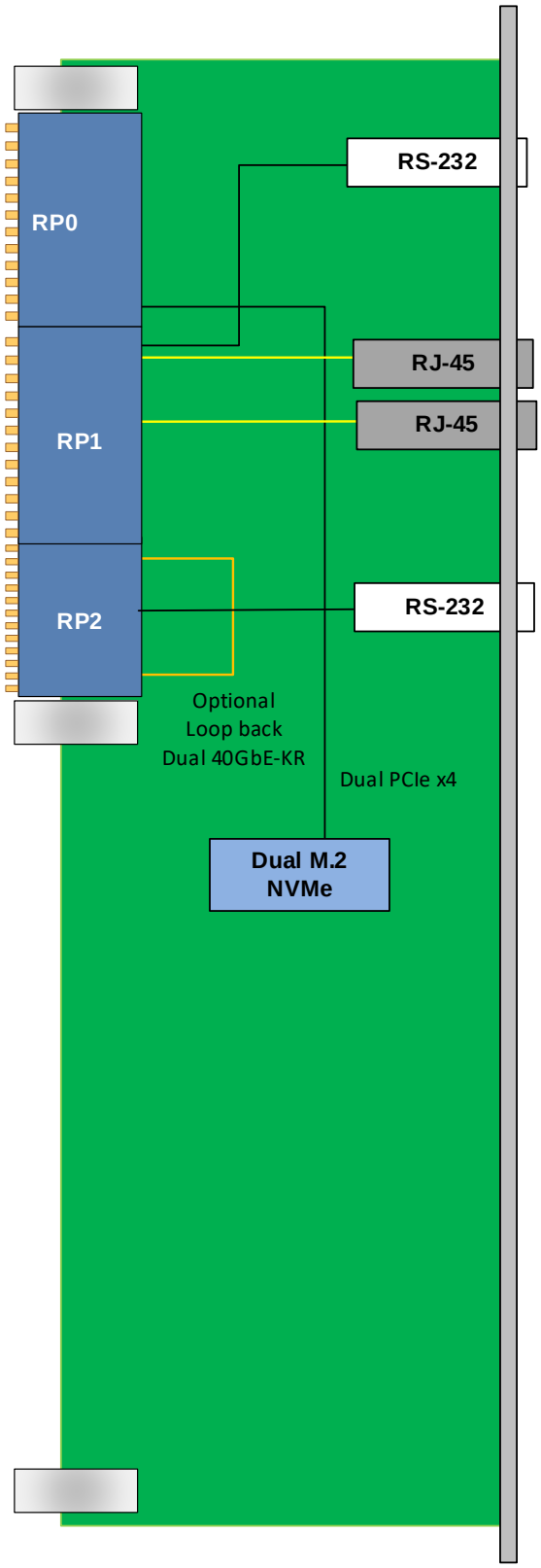

Figure 4: VRT762A Functional Block Diagram

Specifications

Architecture		
Physical	**Dimensions**	6U, 1” pitch
CPU		VPX RTM for VadaTech VPX762 Processor
Configuration		
Power	**VRT762A**	~8W
Memory		None
Rear Panel		RJ-45, Micro USB
VPX Interfaces	**Slot Profiles**	See Ordering Options
Rear IO	**RP0/RP1/RP2**	Dual GbE, RS-232
	Power Supplies	On RP0: +12V/+5V
Other		
MTBF	MIL Hand book 217-F@ TBD hrs	
Certifications	Designed to meet FCC, CE and UL certifications, where applicable	
Standards	VadaTech is certified to both the ISO9001:2015 and AS9100D standards	
Warranty	Two (2) years, see VadaTech Terms and Conditions	

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.

Ordering Options

VRT762A – AB0-000-GHJ

A = Loop Back on the 40GbE ports		G = Applicable Slot Profiles
0 = None 1 = Loop Back Enable		0 = 5 HP
B = M.2 Storage per socket		**H = Environmental**
0 = None 1 = 1 TB 2 = 2 TB 3 = Reserved		See Environmental Specification
		J = Conformal Coating
		0 = No coating 1 = Humiseal 1A33 Polyurethane 2 = Humiseal 1B31 Acrylic

Notes:

Environmental Specification

Air Cooled			Conduction Cooled		
Option H	**H = 0**	**H = 1**	**H = 2**	**H = 3**	**H = 4**
Operating Temperature	AC1* (0°C to +55°C)	AC3* (-40°C to +70°C)	CC1* (0°C to +55°C)	CC3* (-40°C to +70°C)	CC4* (-40°C to +85°C)
Storage Temperature	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C3* (-50°C to +100°C)
Operating Vibration	V2* (0.04 g2/Hz max)	V2* (0.04 g2/Hz max)	V3* (0.1 g2/Hz max)	V3* (0.1 g2/Hz max)	V3 (0.1 g2/Hz max)
Storage Vibration	OS1* (20g)	OS1* (20g)	OS2* (40g)	OS2* (40g)	OS2* (40g)
Humidity	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing

Notes:
*Nomenclature per ANSI/VITA 47. Contact local sales office for conduction cooled (H = 2, 3, 4)

Related Products

VPX516

- 3U FPGA carrier for FPGA Mezzanine Card (FMC) per VITA 46 and VITA 57
- Xilinx Virtex-7 690T FPGA in FFG-1761 package
- High-performance clock jitter cleaner

VPX517

- 3U FPGA carrier for FMC per VITA 46 and VITA 57
- Xilinx Kintex-7 410T FPGA in FFG-900 package
- High-performance clock jitter cleaner

Contact

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