

# Specifications

Architecture	
<b>Physical</b>	<b>Dimensions</b> Double module, mid-size (full-size optional) Width: 5.85" (148.5 mm) Depth 7.11" (180.6 mm)
<b>Type</b>	<b>AMC FPGA Carrier</b> Xilinx Zynq UltraScale+, FMC+ sites
Standards	
<b>AMC</b>	<b>Type</b> AMC.0, AMC.1, AMC.2, AMC.3 and AMC.4
<b>Module Management</b>	<b>IPMI</b> IPMI v2.0
<b>GbE</b>	<b>Lanes</b> Port 0 and 1
<b>PCIe</b>	<b>Lanes</b> x4 (4-7/8-11) or x8 (4-11)
<b>10GbE/40GbE/SRIO</b>	4-7 and 8-11
Configuration	
<b>Power</b>	<b>AMC562</b> ~25W FPGA load dependent (without RTM) and FMC+ <b>To RTM</b> Via Zone 3
<b>Environmental</b>	<b>Temperature</b> See <a href="#">Ordering Options</a> and <a href="#">Environmental Spec Sheet</a> Storage Temperature: -40° to +85°C
	<b>Vibration</b> Operating 9.8 m/s <sup>2</sup> (1G), 5 to 500 Hz on each axis
	<b>Shock</b> Operating 30G on each axis
	<b>Relative Humidity</b> 5 to 95% non-condensing
<b>Front Panel</b>	<b>Interface Connectors</b> FMC+ Slot Min USB port for the RS-232 RJ-45 for ethernet JTAG; 6 SSMC connectors for Clock input, 3x GPI, and 2x GPO
	<b>LEDs</b> IPMI management control Debug (user defined) LED
	<b>Mechanical</b> Hot-swap ejector handle
<b>FMC</b>	<b>On-board</b> FMC+
<b>RTM</b>	<b>Rear</b> Zone 3 Connector (class D1.0 per DESY specification)
<b>Software Support</b>	<b>Operating System</b> Linux
Other	
<b>MTBF</b>	MIL Hand book 217-F@ TBD hrs
<b>Certifications</b>	Designed to meet FCC, CE and UL certifications, where applicable
<b>Standards</b>	VadaTech is certified to both the ISO9001:2000 and AS9100B:2004 standards
<b>Warranty</b>	Two (2) years, see <a href="#">VadaTech Terms and Conditions</a>

## INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.