# NVM257

## IP core for Standalone NVMe Host Controller

## Key Features

- Handles NVMe Protocol in FPGA
- Includes 131 KByte data buffer
- Comprehensive set of NVMe commands (identification, log, format, flush, self-test, read, write, graceful shutdown, etc.)
- Directly applicable to VadaTech FPGA reference design for FMC257

## Benefits

- Reduces development time and cost
- Electrical, mechanical, software, and system-level expertise in house
- Full system supply from industry leader
- AS9100 and ISO9001 certified company

FMC™


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The NVM257 IP core is a standalone NVMe Host Controller with PCIe Bridge and Internal Memory Buffer, designed to handle NVMe Protocol in Xilinx FPGA. This IP core license provides the ability to modify and reuse open-standard Vadatech FPGA reference designs for high performance, high storage capacity, compact NVMe SSDs such as FMC257. It helps reduce development time and cost by providing an extensive functionality to support NVMe without a host processor.

# Specifications

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|---|---|
| **Architecture** | |
| **FPGA IP Core** | NVMe SSD Storage |
| **Standards** | |
| **NVMe** | 1.1 |
| **Deliverable** | |
| **IP Formats** | Encrypted VHDL |
| **Applicable FPGA reference design** | Xilinx Zynq MPSoC UltraScale+ for FMC257 (contact VadaTech Sales) |
| **Other** | |
| **Standards** | VadaTech is certified to both the ISO9001:2000 and AS9100B:2004 standards |
| **Warranty** | One (1) years, see VadaTech Terms and Conditions |

**INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS**

VadaTech has a full ecosystem of ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.

# Ordering Options

**NVM257 – 000-000-000**

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# Related Products

FMC257

- VITA 57.1 FMC with dual NVMe type Solid State Drive (SSD)
- High performance with dual PCIe Gen3 x4 interface
- Includes RS-485/RS-422 or Singled Ended +3.3V IO

VPX586

- Xilinx UltraScale+ XCZU19EG FPGA
- Single FMC+ (VITA 57.4) site
- VITA Open-Standard VPX

AMC560

- Xilinx UltraScale+ XCZU19EG FPGA
- Dual FMC+ (VITA 57.4) sites
- MicroTCA PICMG Open-Standard

# Contact

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- Configurable solutions

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