

SOF218

Quad ADC 14-bit @ 3 GSPS,
Programmable Attenuator, SOFI
Module



SOF218

Key Features

- Quad ADC 14-bit @ 3 GSPS (dual AD9208)
- Programmable attenuation on RF inputs
- Optional additional quad ADC using a further two AD9208
- Onboard Wideband PLL structure
- LMK04821 PLL (with 100 MHz VCXO) for JESD lane clocking and sysref

SOF218 provides four ADC channels via two AD9208s. Optional daughter card DA771 adds another two AD9208s and can support up to eight ADC channels in total. Reference clock is accepted from the front panel via PLL. SOF218 is identical to SOF209 apart from the added feature of ADC input attenuation.

NOTE: This module is not orderable as a standalone item and must be purchased as part of an AMSxxx (AMC module) or VPSxxx (3U VPX module) product. See [SOFI Overview](#) for details.



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Figure 1: SOF218

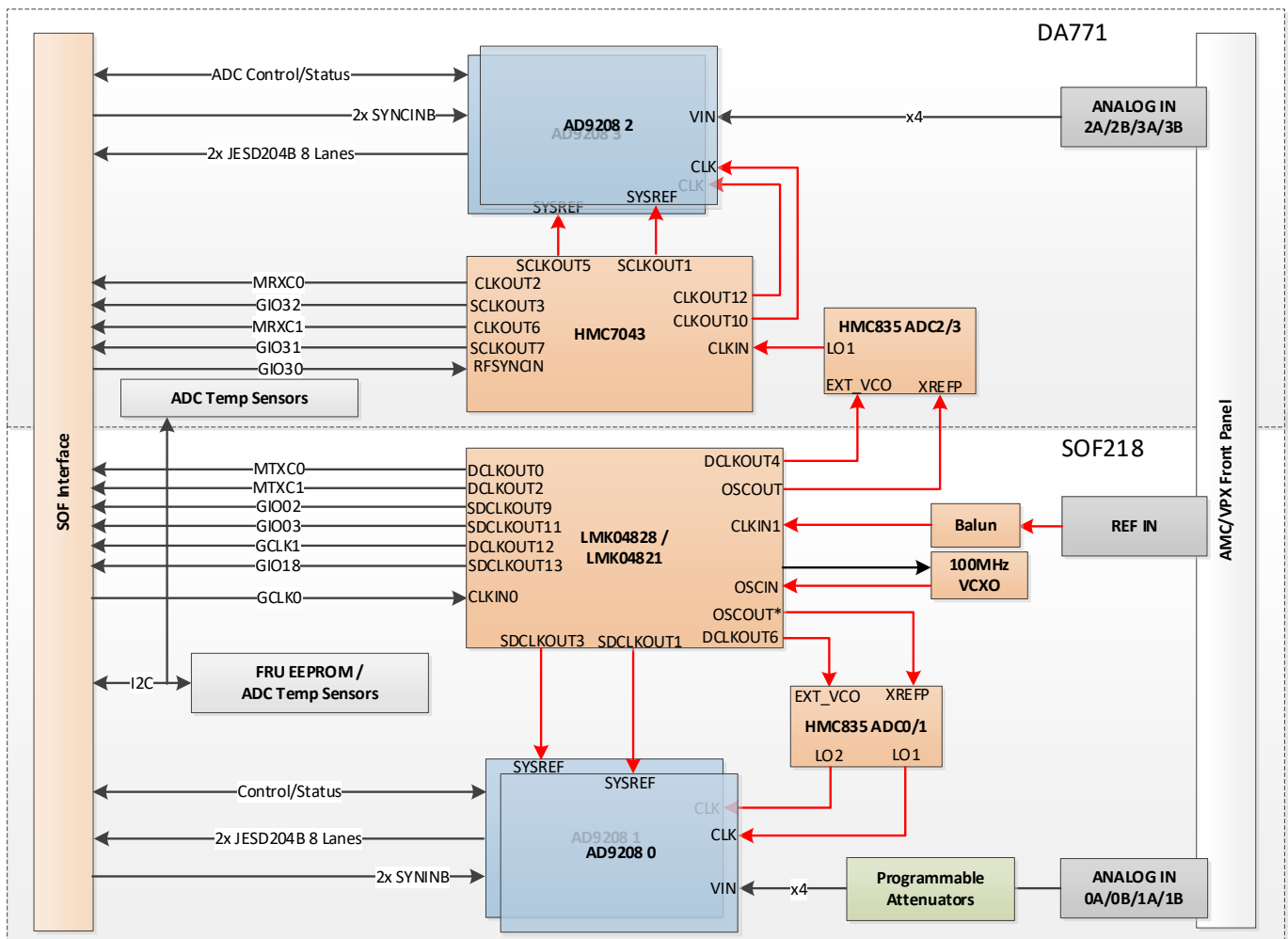


Figure 2: SOF218 and DA771 Functional Block Diagram

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SOFI Modules

SOFI modules are physically similar to FMC modules but are incompatible with FMC carriers, and are not designed to be interchangeable in the field.

See [SOFI Overview](#) for compatibility with AMC and 3U VPX carriers. If the particular combination of form factor, FPGA and ADC/DAC performance you require is not yet listed, please contact your local VadaTech sales team for details of how we can support you.

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