VPX243

3U VPX with 4 SFP+ style ports

Key Features

- 3U VPX form-factor
- x4 SERDES from P1 to four SFP+ style ports
- Allows for 10G down to 1G speed
- Health Management through dedicated Processor

Benefits

- Electrical, mechanical, software, and system-level expertise in house
- Full system supply from industry leader
- AS9100 and ISO9001 certified company
VPX243

The VPX243 is a 3U VPX module per VITA 46. The module brings four SERDES from the P1 connector to four SFP+ style ports in the front. The VPX243 allows for any protocol (Ethernet, Aurora, etc.) to run at 10G down to 1G. The VPX243 has the optical cages recessed to allow for the necessary optical bend radius in a conduction cool chassis.

The module has Clock Data Recovery on board (CDR) for better signal integrity.

Figure 1: VPX243 Front View
## Specifications

### Architecture

| Physical Dimensions | 3U, 1” pitch |

### Standards

| VPX Type | VITA 46.x |
| VPX Type | VITA 65 OpenVPX |

### Module Management

| IPMI | IPMI v2.0 |

### Configuration

| Power | VPX242 | ~6W |
| Front Panel | 4x SFP+ ports |
| Micro USB | RS-232 for Health Management |
| LEDs | User defined by Health Management |
| Onboard Interfaces | SFP+ |

### VPX Interfaces

| Slot Profiles | See Ordering Options |
| Rear IO | P1 SERDES routed from ports 0-3 |

### Power Supplies

| Power Supplies | From P0 |

### Other

| MTBF | MIL Hand book 217-F@ TBD hrs |
| Certifications | Designed to meet FCC, CE and UL certifications, where applicable |
| Standards | VadaTech is certified to both the ISO9001:2015 and AS9100D standards |
| Warranty | Two (2) years, see VadaTech Terms and Conditions |

### INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.
## Ordering Options

**VPX243 – 00C-000-GHJ**

<table>
<thead>
<tr>
<th></th>
<th><strong>G</strong> = Applicable Slot Profile</th>
<th><strong>H</strong> = Environmental</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>0 = 5 HP, VITA 48.1</td>
<td>See Environmental Specification</td>
</tr>
<tr>
<td></td>
<td>1 = Reserved</td>
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</table>

**C = VPX Connector Type**

- 0 = Standard 50u Gold Rugged
- 1 = KVXP Connectors

**J = Conformal Coating**

- 0 = No coating
- 1 = Humiseal 1A33 Polyurethane
- 2 = Humiseal 1B31 Acrylic

### Environmental Specification

**Air Cooled**

<table>
<thead>
<tr>
<th>Option H</th>
<th>H = 0</th>
<th>H = 1</th>
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<tbody>
<tr>
<td>Operating Temperature</td>
<td>AC1* (0°C to +55°C)</td>
<td>AC3* (-40°C to +70°C)</td>
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<tr>
<td>Storage Temperature</td>
<td>C1* (-40°C to +85°C)</td>
<td>C3* (-50°C to +100°C)</td>
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<tr>
<td>Operating Vibration</td>
<td>V2* (0.04 g2/Hz max)</td>
<td>V2* (0.04 g2/Hz max)</td>
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<tr>
<td>Storage Vibration</td>
<td>OS1* (20g)</td>
<td>OS1* (20g)</td>
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<tr>
<td>Humidity</td>
<td>95% non-condensing</td>
<td>95% non-condensing</td>
</tr>
</tbody>
</table>

**Conduction Cooled**

<table>
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<th>H = 3</th>
<th>H = 4</th>
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<tbody>
<tr>
<td>Operating Temperature</td>
<td>CC1* (0°C to +55°C)</td>
<td>CC3* (-40°C to +70°C)</td>
<td>CC4* (-40°C to +85°C)</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>C1* (-40°C to +85°C)</td>
<td>C3* (-50°C to +100°C)</td>
<td>C3* (-50°C to +100°C)</td>
</tr>
<tr>
<td>Operating Vibration</td>
<td>V3* (0.1 g2/Hz max)</td>
<td>V3* (0.1 g2/Hz max)</td>
<td>V3 (0.1 g2/Hz max)</td>
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<tr>
<td>Storage Vibration</td>
<td>OS2* (40g)</td>
<td>OS2* (40g)</td>
<td>OS2* (40g)</td>
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<tr>
<td>Humidity</td>
<td>95% non-condensing</td>
<td>95% non-condensing</td>
<td>95% non-condensing</td>
</tr>
</tbody>
</table>

### Notes:

- *Nomenclature per ANSI/VITA 47. Contact local sales office for conduction cooled (H = 2, 3, 4).
Related Products

VPX516
- 3U FPGA carrier for FPGA Mezzanine Card (FMC) per VITA 46 and VITA 57
- Xilinx Virtex-7 690T FPGA in FFG-1761 package
- High-performance clock jitter cleaner

VPX592
- 3U FPGA carrier for FMC per VITA 46 and VITA 57
- Xilinx Kintex UltraScale™ XCKU115 FPGA
- High-performance clock jitter cleaner

VPX599
- Xilinx Kintex UltraScale™ XCKU115 FPGA
- Dual ADC 12-bit @ 6.4 GSPS
- Dual DAC 16-bit @ 12 GSPS (AD9162 or AD9164)
Contact

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