

Specifications

Architecture			
Physical	Dimensions	6U, VPX	
FPGA		Xilinx Virtex UltraScale+™ XCVU47P	
Configuration			
Power	VPX554	~85W (CPU and FPGA load dependent)	
Front Panel	JTAG	Standard JTAG header via front or P0	
	USB 2.0	RS-232 from Health Management; CPU; FPGA	
	USB 3.0	Dual USB 3.0	
	GbE	RJ-45	
	FMC+0	FMC+ front I/O site 0	
	FMC+1	FMC+ front I/O site 1	
	LEDs	User defined by the FPGA and Health Management	
VPX Interfaces	Slot Profiles	See Ordering Options	
	Rear IO		P1: x4 PCIe Gen3 and x8 Reconfigurable SERDES
			P2: x2 RS422/485
			P3: x12 SERDES (up to 28Gbaud per lane)
			P4: x16 LVDS and/or 32 single ended (+1.8V)
			P5: x48 Single ended configurable as +3.3V or +5V per x8 configuration
		P6 : VITA66.5 12RX/TX	
Software Support	Operating System	Linux (default) or VxWorks	
Other			
MTBF		MIL Hand book 217-F@ TBD hrs	
Certifications		Designed to meet FCC, CE and UL certifications, where applicable	
Standards		VadaTech is certified to both the ISO9001:2015 and AS9100D standards	
Warranty		Two (2) years, see VadaTech Terms and Conditions	

OpenVPX allows for a wide range of pin assignments and use cases. Prior to purchasing VadaTech products as standalone items (i.e. not part of an integrated platform) please consult with VadaTech on the system architecture to ensure compatibility.

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.