## Specifications

Architecture		
Physical	Dimensions	6U, VPX
FPGA		Xilinx Zynq UltraScale+™
Configuration		
Power	VPX556	~25W (FPGA load dependent)
Front Panel	JTAG	JTAG header via front or P0
	USB	USB-to-Serial
	LEDs	User defined by the FPGA and Health Management
VPX Interfaces	Slot Profiles	See Ordering Options
	Rear IO	P1: x14 SERDES; Dual GbE
		P2: x32 LVDS
		P3: x8 LVDS, x12 M-LVDS Out, x12 M-LVDS In
		P4: x11 RS-422 Out, x8 RS-232 In, Clocks/Syncs
		P5: No connection
		P6: VITA 67.2 Clocks
Software Support	<b>Operating System</b>	Linux
Other		
MTBF	MIL Hand book 217-F@ TBD hrs	
Certifications	Designed to meet FCC, CE and UL certifications, where applicable	
Standards	VadaTech is certified to both the ISO9001:2015 and AS9100D standards	
Warranty	Two (2) years, see VadaTech Terms and Conditions	

## INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as preconfigured Application-Ready Platforms. Please contact VadaTech Sales for more information.