

Specifications

Architecture		
Physical	Dimensions	3U, 1" pitch
FPGA		Xilinx UltraScale+™ XCVU13P FPGA
Configuration		
Power	VPX587	~60W (dependent on FPGA load)
Memory		One bank of DDR4, 64-bit wide (8 GB total)
Front Panel		22 Nanonics Coaxial
	Micro USB	RS-232 from Health Management and RS-232 from FPGA
	LEDs	User defined by the FPGA and Health Management
Onboard Interfaces		JTAG
VPX Interfaces	Slot Profiles	See Ordering Options
	Rear IO	x16 SERDES could be configured as PCIe/SRIO/10GbE/40GbE/Aurora on P1 x8 SERDES could be configured as SRIO/10GbE/40GbE/Aurora on P2 x16 LVDS or 32 single-ended on P2
Other		
MTBF		MIL Hand book 217-F@ TBD hrs
Certifications		Designed to meet FCC, CE and UL certifications, where applicable
Standards		VadaTech is certified to both the ISO9001:2015 and AS9100D standards
Warranty		Two (2) years, see VadaTech Terms and Conditions

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.