VPX980

Chassis Manager with JTAG Switch Module (JSM), 6U VPX



Key Features

- Quad ARM Cortex-A53 @ 1.6 GHz per core
- 4GB LP-DDR4 memory
- FRAM for log messages
- 64 NAND Flash
- JSM with 18 JTAG Ports
- I2C Real Time Clock with battery backup
- Low power (4W)
- Based on VadaTech VT040 Module
- IPMI 2.0 compliant

openVP

Benefits

- Supports VITA 46.11 Tier-2 command set
- Utilizing VadaTech VT003 third generation Shelf
- Electrical, mechanical, software, and system-level expertise in house
- Full system supply from industry leader
- AS9100 and ISO9001 certified company



VPX980

The VadaTech VPX980 Chassis Manager is based on the VITA 46.11 specification. The VITA 46.11 leverages the Intelligent Platform Management Interface (IPMI) and AdvancedTCA Specification by PICMG as its architectural foundation. The VPX980 is based on VadaTech VT040 Module VT040 - Shelf Manager, 4th Generation ATCA, MTCA and VPX platforms (vadatech.com).

The VadaTech Chassis Management solution is derived from our field proven VadaTech ATCA Shelf Manager utilizing core interfaces such as the Simple Network Management Protocol (SNMP), Remote Management Control Protocol (RMCP), Web Interface, System Management application (Scorpionware™), and a user-friendly Command Line Interface.

VadaTech VPX IPMC and Chassis Management solutions support VITA 46.11 Tier-2 command set, providing a higher level of functionality in the management layer and chassis cooling capabilities. Additionally, VadaTech VPX management solutions have taken advantage of the HPM.1 PICMG Specification providing a framework for upgrading the IPMC firmware.

The Module has GbE as 1000Base-TX from the front panel connecting to the Shelf Manager. It also has dual GbE to P1 as 1000Base-BX (SERDES Based).

The module also has an option for JTAG Switch Module (JSM) and optional Virtual Probe, easing device access within the chassis for FPGA code developers. The JTAG Ports from each VPX module are routed to the P3/P4 for a total of 18 modules. This allows max flexibility within the chassis. The JSM is configured via either the front panel JTAG Port or the optional Virtual JTAG (VTMJTAG) command line of the management CPU. The optional Virtual JTAG feature is available via either of the GbE Ports (front or rear).

The JSM can also be used as a standalone module, so multiple JTAG dongles are not needed within the chassis.



Figure 1: VPX980

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JTAG Master/JTAG via Ethernet Virtual Probe

The VPX980 provides JTAG Master Capability to send out configuration data streams via the chassis JSM to configure arbitrary JTAG Slave devices on the VPX cards. Virtual Probe services are also available to provide JTAG via Ethernet for Xilinx FPGAs. This allows for standard development tools such as Xilinx iMPACT/ChipScope to treat the MCH/JSM combination as if it was a standard JTAG probe. This approach frees the developer from having to attach JTAG probes directly to the VPX or JSM which can be difficult when systems are already fully assembled. It also allows for remote debugging across long distances when required without the need to install additional JTAG equipment on-site.

Scorpionware[™] Software

VadaTech's Scorpionware [™] software can be used to access information about the current state of the Shelf or the Carrier, obtain information such as the FRU population, or monitor alarms, power management, current sensor values, and the overall health of the Shelf. The software GUI is very powerful, providing a Virtual Carrier and FRU construct for a simple, effective interface.

IPMI Protocol Analyzer

	Platform Event && Request				-	Expressi	on Apply		
No.	Time	Bus	Dir	Src	Dest	Seq	Net Fn	Command	
722	77.050.000	IPMB-A	REQ	0×92	0x20	16	Sensor/Event	Platform Event	
724	77.330.000	IPMB-A	REQ	0×88	0x20	1	Sensor/Event	Platform Event	
725	77.410.000	IPMB-A	REQ	0×90	0x20	20	Sensor/Event	Platform Event	
728	77.740.000	IPMB-B	REQ	0×88	0x20	2	Sensor/Event	Platform Event	
729 730	77.810.000	IPMB-B	REQ	0x92	0x20	20	Sensor/Event	Platform Event	
731	77.830.000 77.840.000	IPMB-A IPMB-B	REQ	0x92 0x92	0x20 0x20	8 12	Sensor/Event Sensor/Event	Platform Event Platform Event	
732	77.870.000	IPMB-6	REQ	0x92 0x92	0x20	16	Sensor/Event	Platform Event	
735	78.210.000	IPMB-A	REO	0x88	0x20	3	Sensor/Event	Platform Event	
736	78.230.000	IPMB-B	REO	0×90	0x20	20	Sensor/Event	Platform Event	
738	78.610.000	IPMB-B	REO	0x68	0×20	4	Sensor/Event	Platform Event	
739	78.640.000	IPMB-B	REQ	0x92	0x20	20	Sensor/Event	Platform Event	
740	78.650.000	IPMB-A	REQ	0x92	0x20	8	Sensor/Event	Platform Event	
741	78.660.000	IPMB-B	REQ	0x92	0x20	12	Sensor/Event	Platform Event	
742	78.690.000	IPMB-A	REQ	0×92	0x20	16	Sensor/Event	Platform Event	
743	79.020.000	IPMB-A	REQ	0×88	0x20	5	Sensor/Event	Platform Event	
744	79.050.000	IPMB-A	REQ	0×90	0x20	20	Sensor/Event	Platform Event	
745 746	79.430.000 79.460.000	IPMB-B IPMB-B	REQ	0x88 0x92	0x20 0x20	6 20	Sensor/Event Sensor/Event	Platform Event Platform Event	
	Request: Ox88 -> O	x20 Plat	form 1	Event	(Sens	or/Eve	nt) (seq 2)		
E	Body								
E	- Body - Event Message	Revision	: 0)x04	(4)				
B	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Revision			(4) (Temper	ature)			
B	- Event Message	Revision	: 0		(Temper	ature)			
E	- Event Message - Sensor Type	Revision	: C : C	0x01 0x02	(Temper				
E	- Event Message - Sensor Type - Sensor Number		: C : C : C)x01)x02)x01	(Temper (2)	old)			
E	Event Message Sensor Type Sensor Number Event Type		: 0 : 0 : 0)x01)x02)x01)x01	(Temper (2) (Thresh (Deasse	old) ction)	itical Going	g High)	
E	- Event Message - Sensor Type - Sensor Number - Event Type - Event Directio	on	: 0 : 0 : 0 : 0)x01)x02)x01)x01)x01)x07	(Temper (2) (Thresh (Deasse	old) ction) Non-Cr:	전망한 것 같은 것 이 것 안 것 같이 같이 같이 같이 같이 같이 많이 많이 많이 많이 많이 많이 했다.	j High)	
E	- Event Message - Sensor Type - Sensor Number - Event Type - Event Directio - Offset	on	: 0 : 0 : 0 : 0 : 0)x01)x02)x01)x01)x07)x07	(Temper (2) (Thresh (Deasse (Upper	old) rtion) Non-Cr: r Read:	ing)	g High)	
E	- Event Message - Sensor Type - Sensor Number - Event Type - Event Directio - Offset - Byte 2 Encodin	on	: 0 : 0 : 0 : 0 : 0 : 0)x01)x02)x01)x01)x07)x07	(Temper (2) (Thresh (Deasse (Upper (Trigge (Trigge	old) rtion) Non-Cr: r Read:	ing)	g High)	
E	- Event Message - Sensor Type - Sensor Number - Event Type - Event Directio - Offset - Byte 2 Encodin - Byte 3 Encodin	on)x01)x02)x01)x01)x07)x07)x01)x01	(Temper (2) (Thresh (Deasse (Upper (Trigge (Trigge (49)	old) rtion) Non-Cr: r Read:	ing)	f High)	

VPX980 can be used as an IPMI protocol analyzer. Figure 2 shows the trace viewer output from VPX980.

Figure 2: IPMI Protocol Analyzer Trace Viewer Output

Block Diagram

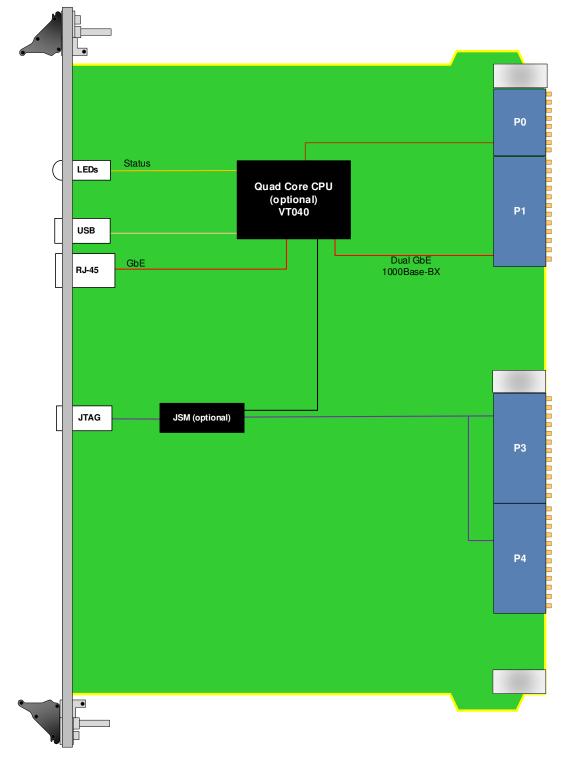
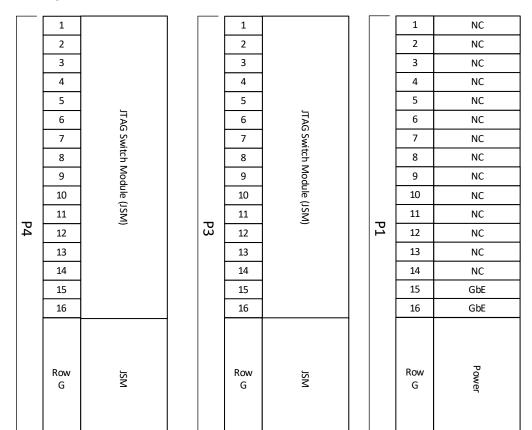


Figure 3: VPX980 Functional Block Diagram

Pinout Block Diagram



Specifications

Architecture		
Physical	Dimensions	6U, 1" pitch
Standards		
Module Management	IPMI	VadaTech VPX Shelf Manager and JTAG Switch Module
Configuration		
Power	VPX980	~4W
Environmental	Temperature	See Ordering Options
Other		
MTBF	MIL Hand book 217-F@ T	BD hrs
Certifications	Designed to meet FCC, CI	E and UL certifications, where applicable
Standards	VadaTech is certified to be	th the ISO9001:2015 and AS9100D standards
Warranty	Two (2) years, see VadaTe	ech Terms and Conditions

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as preconfigured Application-Ready Platforms. Please contact VadaTech Sales for more information.

Ordering Options

VPX980 - ABC-D0F-GHJ

A = Shelf Manager	D = P1 Connector	G = Applicable Slot Profiles	
0 = No Shelf 1 = Reserved 2 = VT040	0 = Installed 1 = Not Installed	0 = 5 HP, VITA 48.1	
B = JSM		H = Environmental	
0 = No JSM 1 = JSM		See Environmental Specification	
C = VPX Connector Type	F = JTAG Virtual Probe*	J = Conformal Coating	
0 = Standard 50u Gold Rugged 1 = KVPX Connectors	0 = No Virtual Probe 1 = Virtual Probe	0 = No coating 1 = Humiseal 1A33 Polyurethane 2 = Humiseal 1B31 Acrylic	

Notes:

*If Option F=1 is required, Option A=2 and B=1 must also be ordered.

Environmental Specification

	Air Cooled		Conduction Cooled			
Option H	H = 0	H = 1	H = 2	H = 3	H = 4	
Operating Temperature	AC1* (0°C to +55°C)	AC3* (-40°C to +70°C)	CC1* (0°C to +55°C)	CC3* (-40°C to +70°C)	CC4* (-40°C to +85°C)	
Storage Temperature	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C3* (-50°C to +100°C)	
Operating Vibration	V2* (0.04 g2/Hz max)	V2* (0.04 g2/Hz max)	V3* (0.1 g2/Hz max)	V3* (0.1 g2/Hz max)	V3 (0.1 g2/Hz max)	
Storage Vibration	OS1* (20g)	OS1* (20g)	OS2* (40g)	OS2* (40g)	OS2* (40g)	
Humidity	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing	

Notes: *Nomenclature per ANSI/VITA 47. Contact local sales office for conduction cooled (H = 2, 3, 4).

Related Products

VPX551



- Dual Kintex UltraScale™ XCKU115
- 16 GB of 64-bit wide DDR4 Memory to each FPGA
- Rear fibre I/O via VITA 66.5

VPX645



VPX752



- 3U VPX NVMe Host Bus Adapter with Full support for RAID
- Dual Core ARM A15 RAID on Chip (ROC)
- Onboard 8 GB of DDR4 Memory with ECC
- 6U VPX module Intel 5th Generation Xeon-D SoC
- PCle Gen3 x16 (dual x8 or quad x4)
- Quad 10GbE XAUI

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