

VRT572C

Rear I/O for VPX572, VPX RTM



VRT572C

Key Features

- 3U RTM per VITA 46
- Digital I/O loop back
- RS-232 from payload
- PCIe Expansion via x4 OCuLink
- LVDS loop back
- LED's
- SERDES loopback
- JTAG

Benefits

- Electrical, mechanical, software, and system-level expertise in house
- Full system supply from industry leader
- AS9100 and ISO9001 certified company



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OpenVPX™



VRT572C

The VRT572C is a 3U VPX Rear Transition Module providing I/O expansion for use with the VPX572.

The VRT572C provides easy access to the I/O ports that are routed to the P1 and P2 connector of the VPX572. These includes loop back for digital input/output, LVDS loop back, SERDES loop back, LEDs to help debug and a JTAG connector.

The module has a PCIe expansion via 4x OCuLink.



Figure 1: VRT572C

Block Diagram

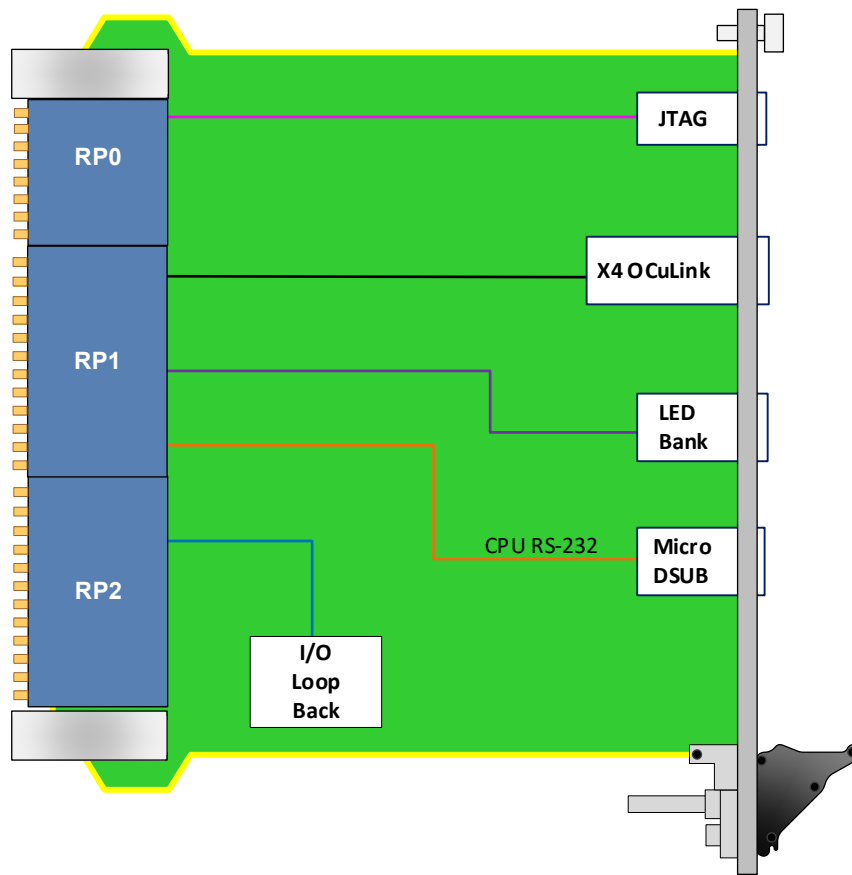


Figure 2: VRT572C Functional Block Diagram

Specifications

| Architecture | | |
|----------------|--|---|
| Physical | Dimensions | 3U RTM, 1" pitch |
| Configuration | | |
| Power | VRT572C | 2W |
| Rear Panel | Connectors | RS-232 via Micro-DSUB |
| | | JTAG |
| | | SERDES for loop back |
| VPX Interfaces | Slot Profiles | See Ordering Options |
| | Backplane | RP0: Power, JTAG |
| | | RP1: SERDES, I/O, RS-232 and LVDS, 4x OCuLink |
| | | RP2: SERDES, I/O and LVDS |
| | | Power Supplies |
| Other | | |
| MTBF | MIL Hand book 217-F@ TBD hrs | |
| Certifications | Designed to meet FCC, CE and UL certifications, where applicable | |
| Standards | VadaTech is certified to both the ISO9001:2015 and AS9100D standards | |
| Warranty | Two (2) years, see VadaTech Terms and Conditions | |

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.

Ordering Options

VRT572C – 000-000-GHJ

G = Applicable Slot Profiles

0 = 5 HP

H = Environmental

See [Environmental Specification](#)

J = Conformal Coating

0 = No coating

1 = Humiseal 1A33 Polyurethane

2 = Humiseal 1B31 Acrylic

Environmental Specification

| | Air Cooled | | | Conduction Cooled | |
|-----------------------|----------------------|-----------------------|----------------------|-----------------------|-----------------------|
| Option H | H = 0 | H = 1 | H = 2 | H = 3 | H = 4 |
| Operating Temperature | AC1* (0°C to +55°C) | AC3* (-40°C to +70°C) | CC1* (0°C to +55°C) | CC3* (-40°C to +70°C) | CC4* (-40°C to +85°C) |
| Storage Temperature | C1* (-40°C to +85°C) | C3* (-50°C to +100°C) | C1* (-40°C to +85°C) | C3* (-50°C to +100°C) | C3* (-50°C to +100°C) |
| Operating Vibration | V2* (0.04 g2/Hz max) | V2* (0.04 g2/Hz max) | V3* (0.1 g2/Hz max) | V3* (0.1 g2/Hz max) | V3 (0.1 g2/Hz max) |
| Storage Vibration | OS1* (20g) | OS1* (20g) | OS2* (40g) | OS2* (40g) | OS2* (40g) |
| Humidity | 95% non-condensing | 95% non-condensing | 95% non-condensing | 95% non-condensing | 95% non-condensing |

Notes:

*Nomenclature per ANSI/VITA 47. Contact local sales office for conduction cooled (H = 2, 3, 4).

Related Products

VPX518



- 3U FPGA carrier for FPGA Mezzanine Card (FMC) per VITA 46 and VITA 57
- Xilinx Zynq-7000 FPGA in FFG-900 package (XC7Z100 or XC7Z045)
- Protocols such as PCIe, SRIO, 10GbE/40GbE, etc. are FPGA programmable

VPX754



- 3U VPX module Intel 5th Generation Xeon D-1577, D-1548 or D-1520 (Broadwell) System-on-Chip (SoC)
- PCIe Gen3 dual x4 or single x8
- Front-panel video out via micro HDMI

VTX870



- Open VPX benchtop development platform
- Dedicated Switch/management slot
- Up to five 3U VPX payload slots

Contact

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DOC NO. 4FM737-12 REV 01 | VERSION 1.1 – JUL/21