Henderson, NV – September 11, 2017 – VadaTech, a leading manufacturer of integrated systems, embedded boards, enabling software and application-ready platforms, announces the FMC216 FPGA Mezzanine Card (FMC) per VITA 57. The FMC216 provides 12-bit ADC conversion at rate of up to 2.6 GSPS (ordering options available for 2.0 and 2.5 GSPS) and 14-bit DAC conversion at rates of up to 5.6 GSPS. The ADC section is based on AD9625 with 8 lanes of JESD204B routed to the FMC connector and full power bandwidth supporting IF sampling of signals up to 2 GHz. The DAC section uses AD9129 which can support multicarrier generation up to 4.2 GHz.

The analog input/output clock and trigger interfaces of the FMC216 are routed via SSMC connectors. The internal clock frequency is programmable and the clock can be locked to an external reference. An ordering option allows separation of ADC and DAC clocks for direct RF synthesis applications.

This product features a high dynamic range for versatility in video/broadcast requirements, which is ideal for communications systems, wireless infrastructure, LTE, ATE, and RADAR/Jamming. The FMC216 is one of over 20 signal conversion FMCs and is compatible with a broad range of both Xilinx and Altera based FMC carriers from VadaTech and others.

About VadaTech

VadaTech provides innovative embedded computing solutions from board-level products, chassis-level platforms, to configurable application-ready systems. With a focus on AdvancedTCA, MicroTCA, VPX and PCIe solutions, the company offers unmatched product selection and expertise. A unique combination of electrical, mechanical, software, and system-level expertise, enables VadaTech to provide customized commercial or rugged computing solutions to meet the most complex customer requirements. VadaTech also offers specialized product solutions for VME, CompactPCI, and other architectures. A member of PICMG and VITA, VadaTech has headquarters, design and manufacturing facilities in Henderson, NV with design, support and sales offices in Europe and Asia Pacific.