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VadaTech Releases Xilinx Zynq FPGA with On-board Clock and Jitter Cleaner

Henderson, NV – Nov 03, 2014 – VadaTech, a manufacturer of embedded boards and complete application-ready platforms, has announced a FPGA in the Advanced Mezzanine Card (AMC) form factor that utilizes the Xilinx® Zynq® all programmable SoC.

The AMC518 from VadaTech comes in a single module, mid-size AMC that allows an FMC per VITA 57 to be plugged into the carrier. The unit has an on-board, re-configurable FPGA which interfaces directly to the AMC FCLKA, TCLKA-D, FMC DP0-7 and all FMC LA/HA/HB pairs. The AMC518 features a clock and jitter cleaner which helps provide clean signals. The FPGA has an interface to a single DDR3 memory channel (64-bit wide). This allows for large buffer sizes to be stored during processing as well as for queuing the data to the host.

The Zynq SoC provides a tight integration of ARM processors and a programmable logic fabric FPGA. This allows a highly efficient design process in an FPGA that offers high-performance with low power consumption.

VadaTech also offers other Xilinx and Altera FPGAs as well as FMC modules. The FMCs include networking, ADC and DAC, and RF module versions.

About VadaTech

VadaTech provides innovative embedded computing solutions from board-level products, chassis-level platforms, to configurable application-ready systems. With a focus on MicroTCA and AdvancedTCA solutions, the company offers unmatched product selection and expertise in the full xTCA ecosystem. With our unique combination of electrical, mechanical, software, and system-level expertise, VadaTech can provide customized commercial or rugged computing solutions to meet the most complex customer requirements. VadaTech also offers specialized product solutions for VPX/VME, CompactPCI, and other architectures. A member of PICMG and VITA, VadaTech is headquartered in Henderson, NV with offices in Europe and Asia Pacific.

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