AMC Packet Processor Quad GbE and/or TOE

AMC222

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KEY FEATURES

- AMC.1 and AMC.2
- Single-width, mid-height (full-height option available)
- Cavium OCTEON CN56xx/CN57xx Multi-Core MIPS64 Processor
- Options for 8 to 12 processor cores
- Options for 600Mhz to 1Ghz speed grade
- DDR2 with ECC memory
- 4 SFP socket supporting GbE with two additional ports routed to the rear
- PCIe x4
- IPMI 2.0 compliant
- RoHS compliant
- OS support for:
  - Linux
  - Windows
  - Solaris
  - VxWorks

The AMC222 is based around the Cavium OCTEON CN56xx/CN57xx processor which has been specifically designed to intelligently process Ethernet packets at line rate. The processor can be loaded via the PCIe interface or via an optional flash memory. The number of processor cores, speed grade, and amount of DDR2 memory is customizable based on customer needs.

The SDK for the processor as well as additional software stacks are available from Cavium or third party. VadaTech also provides the AMC222 pre-configured as a TCP Offload Engine (TOE) to provide socket layer services to a host processor via PCIe. This can dramatically reduce the CPU overhead for TCP packet processing on embedded CPUs. Various flavors of embedded Linux as well as VxWorks are supported.

AdvancedMC™

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SPECIFICATIONS

Architecture		
Physical	**Dimensions**	Mid-Height (Full-Height option)
		Width: 2.89 in. (73.5 mm)
		Depth: 7.11 in. (180.6 mm)
Type	**AMC GbE**	Six port of GbE
Standards		
AMC	**Type**	AMC.1 and AMC.2
Module Management	**IPMI**	IPMI Version 2.0
PCIe	**Lanes**	x4
Configuration		
Power	**AMC222**	~25W (CPU Dependent)
Environmental	**Temperature**	Operating Temperature: 0° to 60° C
		Storage Temperature: -40° to +90° C
	Vibration	1G, 5-500Hz each axis
	Shock	30Gs each axis
	Relative Humidity	5 to 95 percent, non-condensing
Front Panel	**Interface Connectors**	Quad SFP with dual micro USB for USB and Serial
	LEDs	IPMI Management Control
		Activity/Link and two user defined
	Mechanical	Hot Swap Ejector Handle
Software Support	**Operating Systems**	Linux, Windows, Solaris and VxWorks
Other		
MTBF	MIL Spec 217-F >TBD	
Certifications	Designed to meet FCC, CE and UL certifications where applicable	
Standards	VadaTech is certified to both the ISO9001:2000 and AS9100B:2004 standards	
Compliance	RoHS and NEBS	
Warranty	Two (2) years	
Trademarks and Logos	The VadaTech logo is a registered trademark of VadaTech, Inc. Other registered trademarks are the property of their respective owners. AdvancedMC™ and the AdvancedTCA™ logo are trademarks of the PCI Industrial Computers Manufacturers Group. All rights reserved. Specification subject to change without notice.	

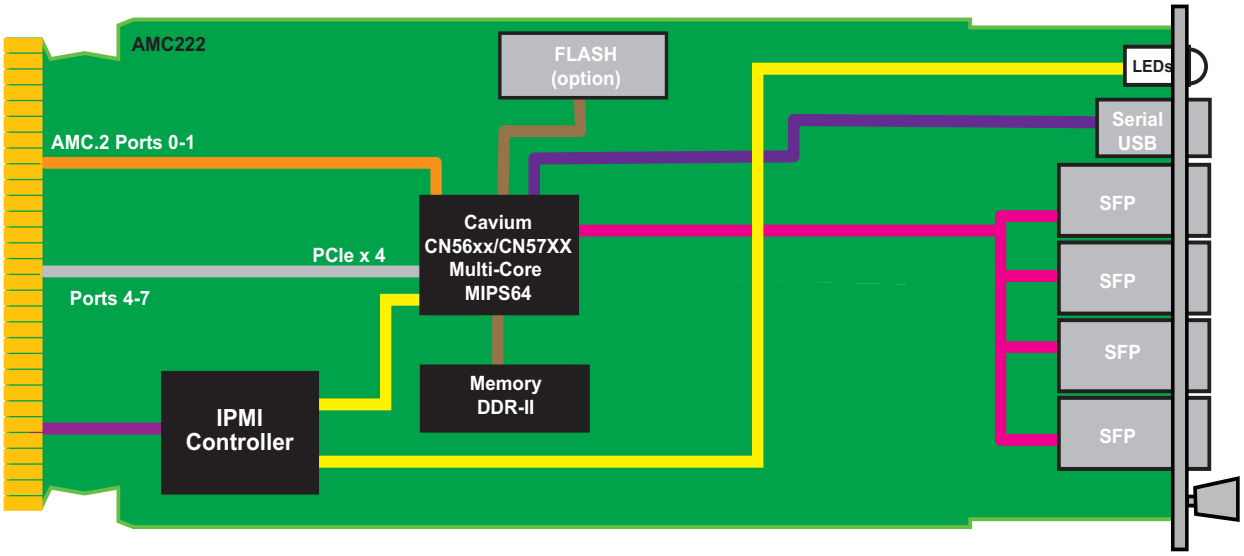

FIGURE 1. AMC222 Functional Block Diagram

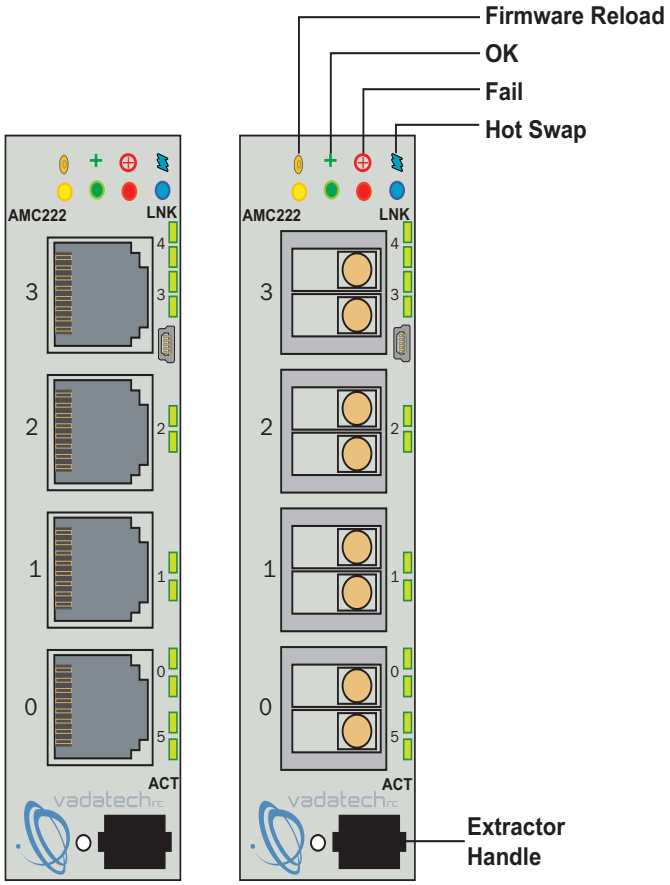

FIGURE 2. AMC222 Front Panel

ORDERING OPTIONS

AMC222 - ABC - DEF - GHJ*

A = CPU

1 = CN56xx (CP)
2 = CN57xx (CP)
3 = CN56xx (NSP)
4 = CN57xx (NSP)

B = Number of CPU Cores

1 = Reserved
2 = 8
3 = 10
4 = 12

C = CPU Speed

1 = 600Mhz
2 = 750Mhz
3 = Reserved
4 = Reserved

D = Memory W/ECC

1 = 1GB
2 = 2GB
3 = 4GB

E = Flash

0 = None
1 = Reserved
2 = Reserved
3 = 64MB
4 = 128MB

F = PCIe Clock

0 = From AMC Clk3
1 = From on board 100 Mhz

G = SFP Transceivers

0 = None
1 = SX 850 nm
2 = LX 1310 nm
3 = Copper RJ-45

H = Pre-configured firmware

0 = None
1 = TOE
2 = Reserved
3 = Reserved

J = Front Panel Height

1 = Reserved
2 = Mid-Height
3 = Full-Height

*Contact VadaTech Sales for Conformal Coating

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