FMC210

FMC High-speed ADC 10-bit @ 2.6 GSPS Module



Key Features

- FPGA Mezzanine Card (FMC) per VITA-57
- Single module
- Single ADC EV10AS150B @ 2.6 GSPS
- 5 GHz Full Power Input Bandwidth (-3dB)
- True single core architecture (no calibration required)
- External Interleaving:
 - Gain Adjust
 - Offset Adjust
 - Sampling Delay Adjust
- Full scale Analog input Voltage Span 500 mVpp
- All front panel input/outputs are via SSMC:
 - Analog Input
 - o Reference clock
 - Trig in/out
- Super low phase noise RF PLL Synthesizer
- RoHS compliant

Benefits

- Array of FMC's and FMC carriers available from VadaTech
- Design utilizes proven VadaTech subcomponents and engineering techniques
- Electrical, mechanical, software, and system-level expertise in house
- Full ecosystem of front and rear boards, enclosures, specialty modules, and test/dev products from one source
- AS9100 and ISO9001 certified company



FMC210

The FMC210 is an FPGA Mezzanine Card per the VITA 57 specification with a high-speed ADC. The ADC converter utilizes the e2v EV10AS150B device which has a high linearity ADC. The module has a super low phase noise RF PLL Synthesizer for sampling.

The ADC device has single Tone Performance in 1st Nyquist (-1 dBFS): ENOB = 8.0 bit, SFDR = -57 dBFS at 2.6 GSPS, Fin = 495 MHz and ENOB = 7.9 bit, SFDR = -57 dBFS at 2.6 GSPS, Fin = 1295 Mhz, with the single Tone Performance in 2nd Nyquist (-3 dB): ENOB = 7.9 bit, SFDR = -59 dBFS at 2.6 GSPS, Fin = 2,595 MHz.

The module has a low Bit Error Rate (BER) of 10 to -12 at 2.6 GSPS with no missing codes at 2.6 GSPS, 1st and 2nd Nyquist.

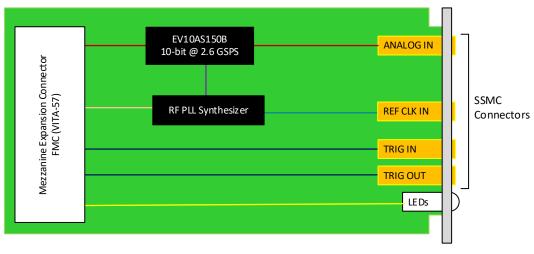
FMC210 has an on board PLL that generates the RF clock. The RF clock could be synced with an external clock or carrier clock.

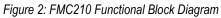


Figure 1: FMC210

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Block Diagram





Front Panel

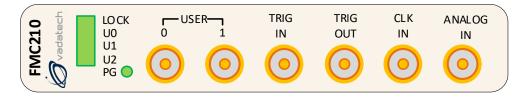


Figure 3: FMC210 Front Panel

Specifications

mm)		
.5 mm)		
Converter (ADC) EV10AS150B		
.5 mm)		
-2008		
<u>otions</u>		
ature: –40° to +85°C		
erating		
on each axis		
each axis		
ndensing		
Panel Connectors		
Polyurethane (Optional)		
Acrylic (Optional)		
MIL Hand book 217-F@ TBD hrs		
Designed to meet FCC, CE and UL certifications, where applicable		
tions, where applicable		
ations, where applicable 2015 and AS9100D standards		

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as preconfigured Application-Ready Platforms. Please contact VadaTech Sales for more information.

Ordering Options

FMC210 - 000-000-GHJ

	G = FMC Board Spacing
	0 = 10 mm (per VITA-57 specification) 1 = 17.5 mm **
	H = Operating Temperature
	0 = Commercial 1 = Industrial
	J = Conformal Coating
Neter	0 = None 1 = Humiseal 1A33 Polyurethane 2 = Humiseal 1B31 Acrylic

Notes:

* Please contact VadaTech for other PLL Synthesizer Frequencies and input clocks.

** For use with carriers that require higher mating clearance, such as VadaTech AMC595. Requires full size AMC.

Related Products





- AMC FPGA carrier for FMC per VITA 57
- Xilinx Zynq-7000 FPGA in FFG-900 package

AMC FPGA carrier for FMC per VITA-57 Xilinx Artix-7 FPGA in FBG-676 package

• Supported by DAQ Series[™] data acquisition software



AMC Ports 0 and 1 as GbE to FPGA





- FPGA Mezzanine Card (FMC) per VITA 57
- Single module AD9739 DAC 14-bit at 2.5 GSPS
- 2 Vpp differential Analog output swing

Contact

VadaTech Corporate Office

198 N. Gibson Road, Henderson, NV 89014 Phone: +1 702 896-3337 | Fax: +1 702 896-0332

Asia Pacific Sales Office

7 Floor, No. 2, Wenhu Street, Neihu District, Taipei 114, Taiwan Phone: +886-2-2637-7655 | Fax: +886-2-2637-7792

VadaTech European Sales Office

VadaTech House, Bulls Copse Road, Southampton, SO40 9LR Phone: +44 2380 016403

info@vadatech.com | www.vadatech.com

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