

SOF200

Dual ADC @ 6.4 GSPS, SOFI Module



SOF200

Key Features

- Dual ADC 12-bit @ 6.4 GSPS (dual ADC12DJ3200), supports four channels @ 3.2 GSPS
- Dual DAC 16-bit @ 12 GSPS (dual AD9162 or AD9164), supports 6 GHz direct RF synthesis
- Sampling clock from host module
- Onboard Wideband PLL structure
- LMK04821 PLL (with 100 MHz VCXO) for JESD lane clocking and sysref

The SOF200 provides dual ADC sampling rates of up to 6.4 GSPS at a 12-bit resolution (TI ADC12DJ3200 or ADC12DJ2700) or quad inputs at 3.2 GSPS. Also, dual DAC delivers update rates of up to 12 GSPS and incorporates direct RF synthesis capable of 6 GSPS at a 16-bit resolution (Analog Devices AD9162 or AD9164). This makes the module suitable for signal capture/analysis applications such as COMINT/SIGINT, radar, research and instrumentation.

NOTE: This module is not orderable as a standalone item and must be purchased as part of an AMSxxx (AMC module) or VPSxxx (3U VPX module) product. See [SOFI Overview](#) for details.



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Figure 1: SOF200

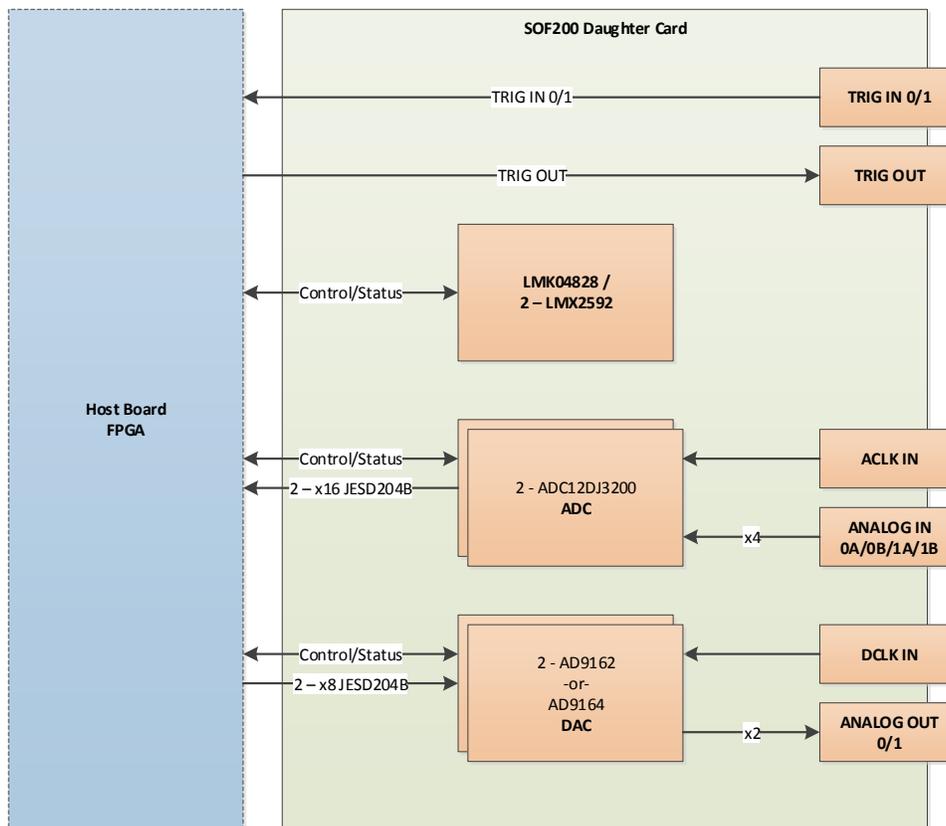


Figure 2: SOF200 Functional Block Diagram

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SOFI Modules

SOFI modules are physically similar to FMC modules but are incompatible with FMC carriers, and not designed to be interchangeable in the field.

See [SOFI Overview](#) for compatibility with AMC and 3U VPX carriers. If the particular combination of form factor, FPGA and ADC/DAC performance you require is not yet listed, please contact your local VadaTech sales team for details of how we can support you.

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