VRT540A

VPX Rear Transition Module with 5x QSFP28 and I/O for 6U VPX

VRT540A

Key Features

- 6U RTM VITA 46
- 5x QSFP28 Ports
- Dual 1000BASE-T GbE
- GPIO and LVDS to High Density Connector
- Interfaces with the VadaTech VPX VPX540

Benefits

- Full System supply from industry leader
- AS9100 and ISO9001 certified company

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VRT540A

The VRT540A is a 6U VPX Rear Transition Module for use with the VadaTech VPX540. The module has 5 x QSFP28+ ports. The ports are configurable by the software and can be organized as 100GbE or any other protocol.

The VRT540 also routes the VPX540 Dual 1000BASE-T GbE ports to dual RJ-45. The RP3 which carries the GPIO and LVDS are routed to a high-density connector.

Figure 1: VRT540A

Figure 2: VRT540A Top View
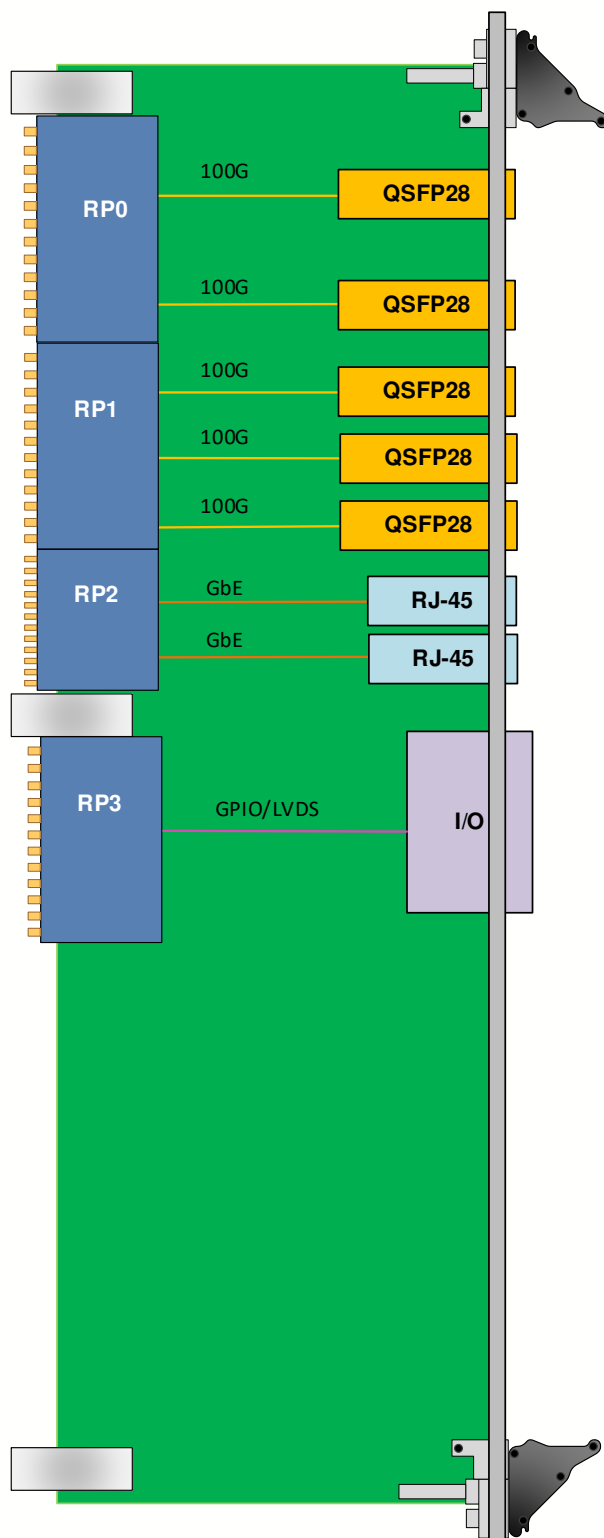
Figure 3: VRT540A Front Panel View

Block Diagram

Figure 4: VRT540A Functional Block Diagram

Specifications

Architecture		
Physical	**Dimensions**	6U, 1" pitch
FPGA		VPX RTM
Configuration		
Power	**VRT540A**	~4W (without Fibre Transceivers)
Memory		None
Rear Panel		5x QSFP28 for 100GbE and/or any protocol with Dual 1000BASE-T GbE
VPX Interfaces	**Slot Profiles**	See Ordering Options
Rear IO	**RP3**	GPIO and LVDS
	Power Supplies	On RP0: 12V
Other		
MTBF		MIL Hand book 217-F@ TBD hrs
Certifications		Designed to meet FCC, CE and UL certifications, where applicable
Standards		VadaTech is certified to both the ISO9001:2015 and AS9100D standards
Warranty		Two (2) years, see VadaTech Terms and Conditions

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.

Ordering Options

VRT540A – A00-000-GHJ

A = QSFP28 Transceivers*		G = Applicable Slot Profiles
0 = None 1 = SR 2 = LR (1Km) 3 = WDM with LC 4 = Reserved 5 = Reserved		0 = 5 HP
		H = Environmental
		See Environmental Specification
		J = Conformal Coating
		0 = No coating 1 = Humiseal 1A33 Polyurethane 2 = Humiseal 1B31 Acrylic

Notes:

*All 5 Transceivers will be the same. Contact local sales office for mix of options.

Environmental Specification

Option H	Air Cooled		Conduction Cooled		
	H = 0	**H = 1**	**H = 2**	**H = 3**	**H = 4**
Operating Temperature	AC1* (0°C to +55°C)	AC3* (-40°C to +70°C)	CC1* (0°C to +55°C)	CC3* (-40°C to +70°C)	CC4* (-40°C to +85°C)
Storage Temperature	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C3* (-50°C to +100°C)
Operating Vibration	V2* (0.04 g2/Hz max)	V2* (0.04 g2/Hz max)	V3* (0.1 g2/Hz max)	V3* (0.1 g2/Hz max)	V3 (0.1 g2/Hz max)
Storage Vibration	OS1* (20g)	OS1* (20g)	OS2* (40g)	OS2* (40g)	OS2* (40g)
Humidity	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing

Notes:

*Nomenclature per ANSI/VITA 47. Contact local sales office for conduction cooled (H = 2, 3, 4)

Related Products

VPX516

- 3U FPGA carrier for FPGA Mezzanine Card (FMC) per VITA 46 and VITA 57
- Xilinx Virtex-7 690T FPGA in FFG-1761 package
- High-performance clock jitter cleaner

VPX517

- 3U FPGA carrier for FMC per VITA 46 and VITA 57
- Xilinx Kintex-7 410T FPGA in FFG-900 package
- High-performance clock jitter cleaner

Contact

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