

VRT571A

Rear I/O for VPX571, VPX RTM



VRT571A

Key Features

- 3U RTM per VITA 46
- Dual GbE
- VPX clock inputs
- High density connector for I/O
- Management and payload RS-232 ports
- JTAG connector

Benefits

- Electrical, mechanical, software, and system-level expertise in house
- Full system supply from industry leader
- AS9100 and ISO9001 certified company

OpenVPX[™]



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VRT571A

The VRT571A is a 3U VPX Rear Transition Module providing I/O expansion for use with the VPX571.

Dual GbE UTP are routed from RP1 to back panel RJ-45. The VPX dual clock input signals are routed from the back panel to the RTM to allow front module input.

The high speed I/O connector supports general purpose and serial interfaces routed from RP1 LVDS. This section of the board can be customized for specific customer requirements, contact VadaTech sales for details.

VRT571A also has a JTAG connector.



Figure 1: VRT571A

Block Diagram

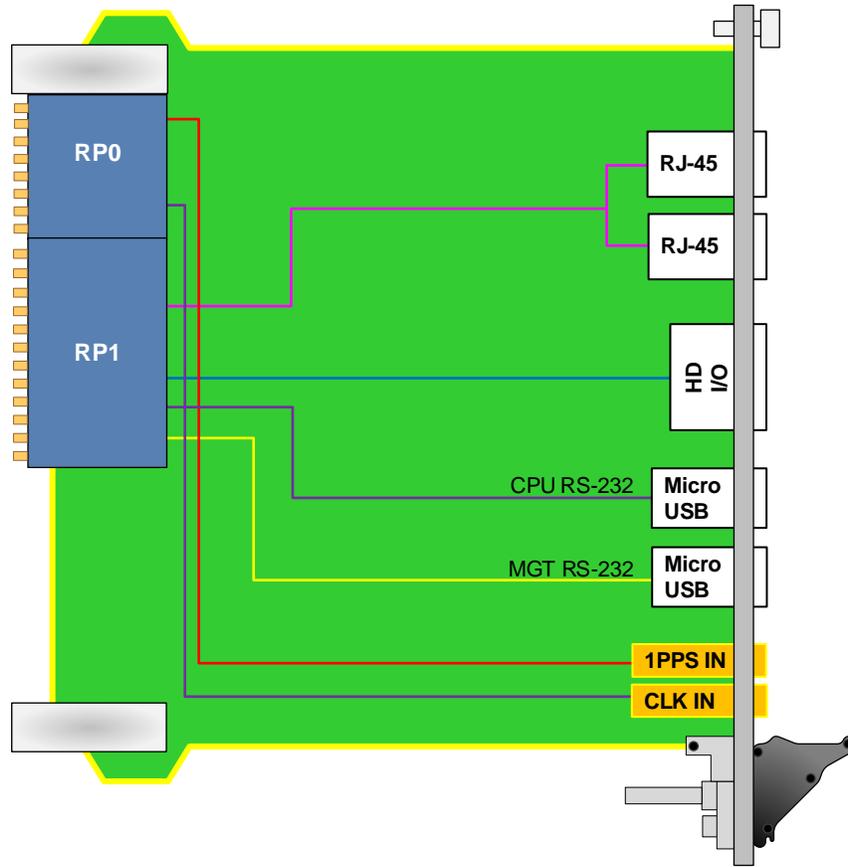


Figure 2: VRT571A Functional Block Diagram

Back Panel

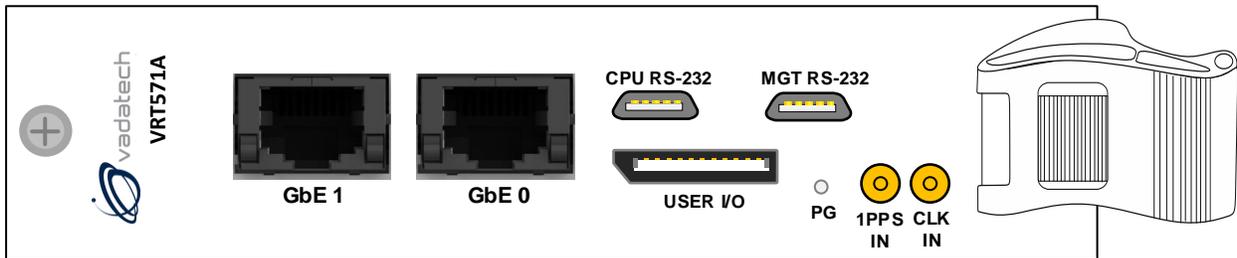


Figure 3: VRT571A Back Panel

Specifications

Architecture	
Physical	Dimensions 3U RTM, 1" pitch
Configuration	
Power	VRT571A 3W
Rear Panel	Connectors VPX clock via SMPM Dual GbE, RJ-45 GPIO/Serial, Oculink
VPX Interfaces	Slot Profiles See ordering options
	Backplane RP0: 1 PPS RP1: Dual GbE RP1: I/O
	Power Supplies RP0 +5V and +3.3V
Other	
MTBF	MIL Hand book 217-F@ TBD hrs
Certifications	Designed to meet FCC, CE and UL certifications, where applicable
Standards	VadaTech is certified to both the ISO9001:2000 and AS9100B:2004 standards
Warranty	Two (2) years

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.

Ordering Options

VRT571A – 000-00-GHJ

G = Applicable Slot Profiles

0 = 5 HP

H = Environmental

See Environmental Specification Table below

J = Conformal Coating

0 = No coating
 1 = Humiseal 1A33 Polyurethane
 2 = Humiseal 1B31 Acrylic

Environmental Specification

Option H	Air Cooled		Conduction Cooled		
	H = 0	H = 1	H = 2	H = 3	H = 4
Operating Temperature	AC1* (0°C to +55°C)	AC3* (-40°C to +70°C)	CC1* (0°C to +55°C)	CC3* (-40°C to +70°C)	CC4* (-40°C to +85°C)
Storage Temperature	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C3* (-50°C to +100°C)
Operating Vibration	V2* (0.04 g2/Hz max)	V2* (0.04 g2/Hz max)	V3* (0.1 g2/Hz max)	V3* (0.1 g2/Hz max)	V3 (0.1 g2/Hz max)
Storage Vibration	OS1* (20g)	OS1* (20g)	OS2* (40g)	OS2* (40g)	OS2* (40g)
Humidity	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing

Notes: *Nomenclature per ANSI/VITA 47. Contact local sales office for conduction cooled (H = 2, 3, 4)

Related Products

VPX518



- 3U FPGA carrier for FPGA Mezzanine Card (FMC) per VITA 46 and VITA 57
- Xilinx Zynq-7000 FPGA in FFG-900 package (XC7Z100 or XC7Z045)
- Protocols such as PCIe, SRIO, 10GbE/40Gbe, etc. are FPGA programmable

VPX754



- 3U VPX module Intel 5th Generation Xeon D-1577, D-1548 or D-1520 (Broadwell) System-on-Chip (SoC)
- PCIe Gen3 dual x4 or single x8
- Front-panel video out via micro HDMI

VTX870



- Open VPX benchtop development platform
- Dedicated Switch/management slot
- Up to five 3U VPX payload slots

Contact

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