VRT771A

RTM for VPX771, 3U VPX

VRT771A

Key Features

- 3U Rear Transition Module (RTM) VITA 46
- Dual GbE 1000BASE-T
- RS-232 from CPU, Health Management and BMC
- M.2 Socket
- Loop back on the 40GBase-KR

Benefits

- Integrated Systems
- Full System supply from industry leader
- AS9100 and ISO9001 certified company

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VRT771A

The VRT771A is a 3U VPX Rear Transition Module (RTM) for use with the VPX771 module. The RTM has an M.2 socket, dual 1000BASE-T, RS-232 from the CPU/Health Management and BMC. It provides a loop back of the 40GBase-KR for test and validation.

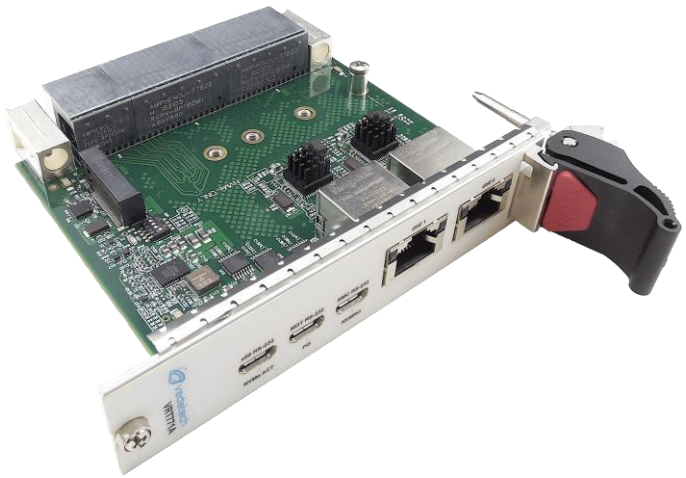

Figure 1: VRT771A

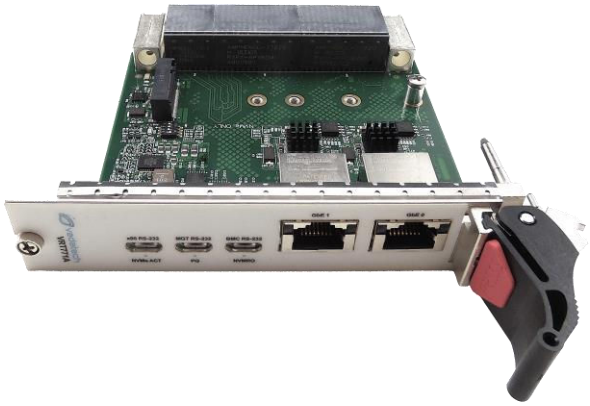

Figure 2: VRT771A Top View

Figure 3: VRT771A Front Panel View

Block Diagram

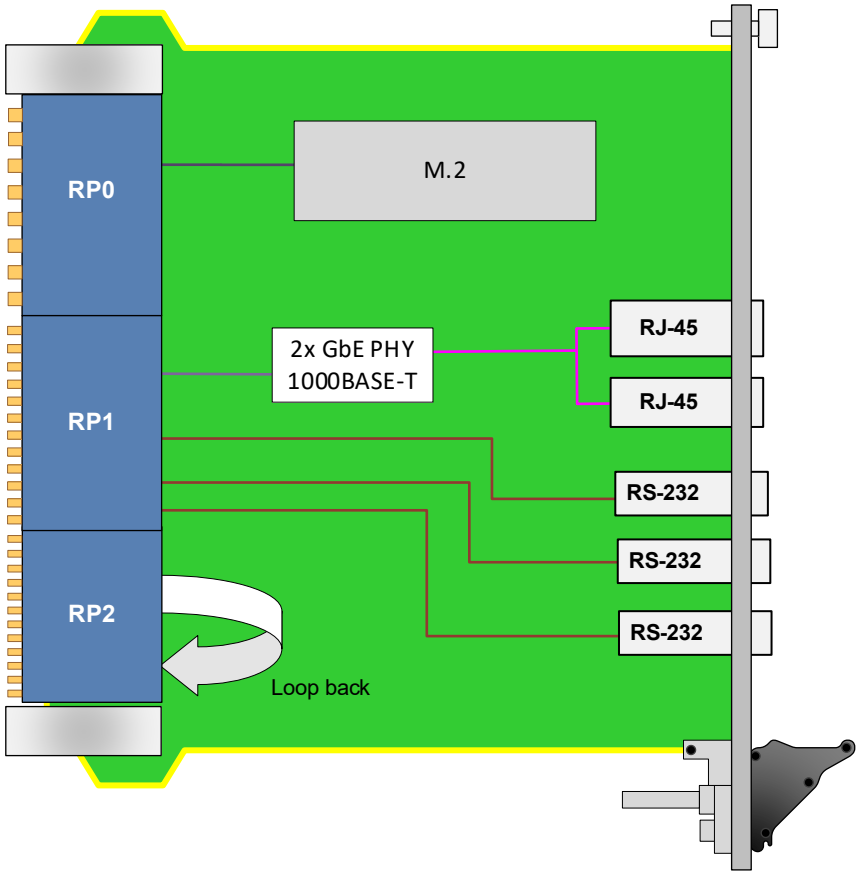

Figure 4: VRT771A Functional Block Diagram

Specifications

Architecture		
Physical	**Dimensions**	3U, 1” pitch
		VPX RTM
Configuration		
Power	**VRT771A**	4W (with M.2)
Memory		None
Rear Panel		2x 1000BASE-T with 3x RS-232
VPX Interfaces	**Slot Profiles**	See Ordering Options
Rear IO		2x RJ-45 and 3x Micro USB
	Power Supplies	On RP0: 12V
Other		
MTBF	MIL Hand book 217-F@ TBD hrs	
Certifications	Designed to meet FCC, CE and UL certifications, where applicable	
Standards	VadaTech is certified to both the ISO9001:2015 and AS9100D standards	
Warranty	Two (2) years, see VadaTech Terms and Conditions	

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.

Ordering Options

VRT771A – AB0-000-GHJ

A = M.2		G = Applicable Slot Profiles
0 = None 1 = 1TB 2 = 2TB		0 = 5 HP
B = Loop back on ports 9-16 of P2		**H = Environmental**
0 = Ports 9-12 to 13-16 1 = No loop back		See Environmental Specification
		J = Conformal Coating
		0 = No coating 1 = Humiseal 1A33 Polyurethane 2 = Humiseal 1B31 Acrylic 3 = Parylene

Notes:

Environmental Specification

Air Cooled			Conduction Cooled		
Option H	**H = 0**	**H = 1**	**H = 2**	**H = 3**	**H = 4**
Operating Temperature	AC1* (0°C to +55°C)	AC3* (-40°C to +70°C)	CC1* (0°C to +55°C)	CC3* (-40°C to +70°C)	CC4* (-40°C to +85°C)
Storage Temperature	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C3* (-50°C to +100°C)
Operating Vibration	V2* (0.04 g2/Hz max)	V2* (0.04 g2/Hz max)	V3* (0.1 g2/Hz max)	V3* (0.1 g2/Hz max)	V3 (0.1 g2/Hz max)
Storage Vibration	OS1* (20g)	OS1* (20g)	OS2* (40g)	OS2* (40g)	OS2* (40g)
Humidity	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing

Notes:

*Nomenclature per ANSI/VITA 47. Contact local sales office for conduction cooled (H = 2, 3, 4)

Related Products

VPX516

- 3U FPGA carrier for FPGA Mezzanine Card (FMC) per VITA 46 and VITA 57
- Xilinx Virtex-7 690T FPGA in FFG-1761 package
- High-performance clock jitter cleaner

VPX517

- 3U FPGA carrier for FMC per VITA 46 and VITA 57
- Xilinx Kintex-7 410T FPGA in FFG-900 package
- High-performance clock jitter cleaner

Contact

VadaTech Corporate Office

198 N. Gibson Road, Henderson, NV 89014
Phone: +1 702 896-3337 | Fax: +1 702 896-0332

Asia Pacific Sales Office

7 Floor, No. 2, Wenhu Street, Neihu District, Taipei 114, Taiwan
Phone: +886-2-2627-7655 | Fax: +886-2-2627-7792

VadaTech European Sales Office

VadaTech House, Bulls Copse Road, Southampton, SO40 9LR
Phone: +44 2380 016403

info@vadatech.com | www.vadatech.com

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DOC NO. 4FM737-12 REV 01 | VERSION 1.3 – JUL/25