

VRT772A

RTM for VPX772, 3U VPX



VRT772A

Key Features

- 3U Rear Transition Module (RTM) VITA 46
- GbE 1000BASE-T and 2x USB2.0/3.0
- RS-232 from CPU and Health Management
- M.2 Socket
- Loop back on the 40GBase-KR

Benefits

- Integrated Systems
- Full System supply from industry leader
- AS9100 and ISO9001 certified company



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OpenVPX™



VRT772A

The VRT772A is a 3U VPX Rear Transition Module (RTM) for use with the VPX772 module. The RTM has an M.2 socket, 1000BASE-T, dual USB2.0/3.0 and RS-232 from the CPU and Health Management. It provides a loop back on the 40GBase-KR for test and validation.



Figure 2: VRT772A



Figure 2: VRT772A Top View



Figure 3: VRT772A Front Panel View

Block Diagram

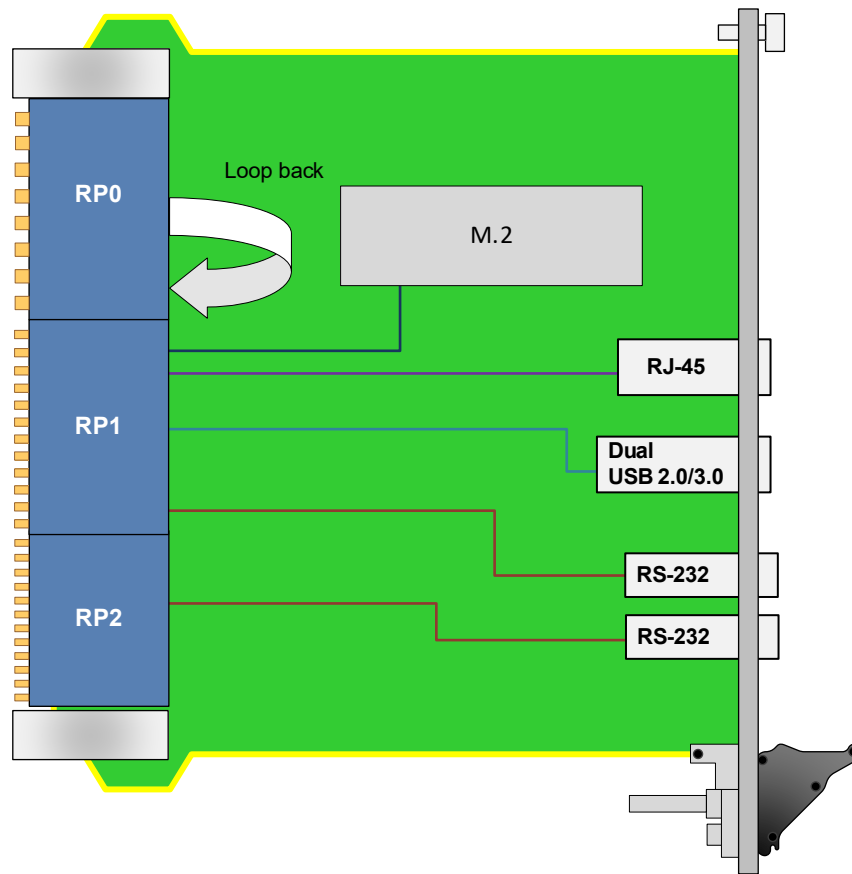


Figure 4: VRT772A Functional Block Diagram

Specifications

Architecture		
Physical	Dimensions	3U, 1" pitch
		VPX RTM
Configuration		
Power	VRT772A	4W (with M.2)
Memory		None
Rear Panel		1000BASE-T, 2x RS-232 and 2x USB 2.0/3.0
VPX Interfaces	Slot Profiles	See Ordering Options
Rear IO		RJ-45, 2x Micro USB and 2x USB type A
	Power Supplies	On RP0: 12V
Other		
MTBF		MIL Hand book 217-F@ TBD hrs
Certifications		Designed to meet FCC, CE and UL certifications, where applicable
Standards		VadaTech is certified to both the ISO9001:2015 and AS9100D standards
Warranty		Two (2) years, see VadaTech Terms and Conditions

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.

Ordering Options

VRT772A – AB0-000-GHJ

A = M.2 0 = None 1 = 1TB 2 = 2TB		G = Applicable Slot Profiles 0 = 5 HP
B = P1 Ports 1-4 loopback to 5-8 0 = No loopback 1 = Loopback		H = Environmental See Environmental Specification
		J = Conformal Coating 0 = No coating 1 = Humiseal 1A33 Polyurethane 2 = Humiseal 1B31 Acrylic 3 = Parylene

Notes:

Environmental Specification

Air Cooled			Conduction Cooled		
Option H	H = 0	H = 1	H = 2	H = 3	H = 4
Operating Temperature	AC1* (0°C to +55°C)	AC3* (-40°C to +70°C)	CC1* (0°C to +55°C)	CC3* (-40°C to +70°C)	CC4* (-40°C to +85°C)
Storage Temperature	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C3* (-50°C to +100°C)
Operating Vibration	V2* (0.04 g2/Hz max)	V2* (0.04 g2/Hz max)	V3* (0.1 g2/Hz max)	V3* (0.1 g2/Hz max)	V3 (0.1 g2/Hz max)
Storage Vibration	OS1* (20g)	OS1* (20g)	OS2* (40g)	OS2* (40g)	OS2* (40g)
Humidity	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing

Notes:

*Nomenclature per ANSI/VITA 47. Contact local sales office for conduction cooled (H = 2, 3, 4)

Related Products

VPX516



- 3U FPGA carrier for FPGA Mezzanine Card (FMC) per VITA 46 and VITA 57
- Xilinx Virtex-7 690T FPGA in FFG-1761 package
- High-performance clock jitter cleaner

VPX517



- 3U FPGA carrier for FMC per VITA 46 and VITA 57
- Xilinx Kintex-7 410T FPGA in FFG-900 package
- High-performance clock jitter cleaner

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